Exhibit #	Description	Page Admitted/ Marked	Date Admitted/ Marked	Transcript Page Where Exhibit is Mentioned/JX#/Notes	In Camera?	<u>Cross-</u> Reference
CX0003	JC-42.3 Committee on Bipolar, Minutes of Meeting No. 43 (Washington, DC)	2604	5/21/2003	310:20; 583:22	<u> </u>	
CX0013	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 6 (San Diego, CA)	2604	5/21/2003	JXA		
CX0018	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 7 (Anchorage, AK)	2604	5/21/2003	JXA		
CX0020	Minutes of Meeting on high performance DRAM architecture (Boxborough, MA)	1372	5/8/2003	1369:22; 1372:7		
CX0021	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 8 (Philadelphia, PA)	2604	5/21/2003	2433:19; 2434:8; 5030:14,24; 5032:19		
CX0027A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 60 (Maui, HI); produced by Rambus	2604	5/21/2003	JXA		
CX0030	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 9 (Seattle, WA)	2604	5/21/2003	5038:25		
CX0031A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 61 (Seattle, WA); produced by Rambus	2604	5/21/2003	JXA		
CX0034	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 62 (New Orleans, LA)	450	5/1/2003	440:4,8,9; 441:15; 450:9; 567:10; 1384:9; 1386:21; 2510:15,22; 2512:14,16; 2516:2; 2524:12,16; 2539:14,25; 2542:7; 3054:18; 3061:17; 5103:4,11,17; 5173:25; 5174:3,7; 5542:10; 7931:19; 7932:3,5,12; 7936:13; 9024:17		
CX0034	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 62 (New Orleans, LA)	2604	5/21/2003	JXA		
CX0034A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 62 (New Orleans, LA); produced by Rambus	2604	5/21/2003	JXA		
CX0035	JEDEC Council Minutes of Meeting No. 90 (Dallas, TX)	2604	5/21/2003	2389:11,19; 2391:11; 2393:2; 2411:25; 2516:4,8,14,18		
CX0037A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 63 (Denver, CO); produced by Rambus	2604	5/21/2003	7912:8		
CX0039	JEDEC Council Minutes of Meeting No. 91 (Washington, DC)	2604	5/21/2003	2413:17		
CX0040	JC-16 Minutes of Meeting No. 6 Voltage Level and ElecCi28 3eTD 0.03351 Tw (ElecCi28 3eTD 0dface	eMinutes of (Cryst 3	eCity, V1 Tc 0 Tw	v (450)4) Tj 63.12 0 TD -0.0374 9765/1/2003		

	Description	Page Admitted/	Date Admitted/	Transcript Page Where Exhibit is  Mentioned/JX#/Notes	In Camera?	Cross-
Exhibit # CX0042	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 64 (Crystal City, VA)	Marked 336	<u>Marked</u> 5/1/2003	326:13,15,23; 329:16; 332:7,22; 334:21; 335:3; 336:19; 10971:24; 10980:9	in Camera?	Reference
CX0042	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 64 (Crystal City, VA)	2604	5/21/2003	JXA		
CX0042A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 64 (Crystal City, VA); produced by Rambus	2604	5/21/2003	7942:1; 7943:2,7		
CX0044	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 13 (Ft. Lauderdale, FL)	9270	7/17/2003	9270:12,22		
CX0045A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 65 (Ft. Lauderdale, FL); produced by Rambus	2604	5/21/2003	JXA		
CX0046	JEDEC Council Minutes of Meeting No. 92 (Las Vegas, NV)	2497	5/21/2003	2420:1,10; 2497:12; 2677:9		
CX0047	JC-42.3 Task Group Minutes, Meeting on Synchronous DRAM Ballots (Las Vegas, NV)	2604	5/21/2003	JXA		
CX0047A	JC-42.3 Task Group Minutes, Meeting on Synchronous DRAM Ballots (Las Vegas, NV); produced by Rambus	2604	5/21/2003	JXA		
CX0050A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 66 (Scottsdale, AZ); produced by Rambus	2604	5/21/2003	JXA		
CX0052	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 15 (Chicago, IL)	2604	5/21/2003	JXA		
CX0053A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 67 (Chicago, IL); produced by Rambus	2604	5/21/2003	JXA		
CX0054	JEDEC Council Minutes of Meeting No. 93 (Boston, MA)	2597	5/21/2003	2423:8,17; 2497:18; 2558:20; 2559:4		
CX0055	JEDEC Council Minutes of Meeting No. 94 (Denver, CO)	n/a	pending	Joint Stipulation filed with the Court on September 29, 2003 - order pending		
CX0057A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 68 (Boston, MA); produced by Rambus	2604	5/21/2003	JXA		
CX0060A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 69 (San Diego, CA); produced by Rambus	2604	5/21/2003	JXA		
CX0064A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 70 (Orlando, FL); produced by Rambus	2604	5/21/2003	JXA		

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CX0074A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 72 (Albuquerque, NM); produced by Rambus	2604	5/21/2003	JXA	iii Camera:	Kelerence
CX0079A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 73 (Maui, HI); produced by Rambus	2604	5/21/2003	JXA		
CX0080A	JC-42.3 DRAM Task Group Meeting (San Jose, CA); produced by Rambus	2604	5/21/2003	JXA		
CX0082	JC-16 Minutes of Meeting No. 17, Voltage Level and Electrical Interface Committee (Las Vegas, NV)	3352	5/29/2003	3239:12; 3352:15		
CX0083	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 23 (Las Vegas, NV)	2604	5/21/2003	3238:24; 5214:14; 5221:16		
CX0084A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 74 (Las Vegas, NV); produced by Rambus	2604	5/21/2003	JXA		
CX0088A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 75 (New Orleans, LA)	2604	5/21/2003	3251:3		
CX0091A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 76 (Crystal City, VA); produced by Rambus	2604	5/21/2003	7770:24; 7771:4; 7772:15; 8389:18		
CX0097	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 26 (Dallas, TX)	1702	5/13/2003	1700:24; 1701:3; 1702:15		
CX0098A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 77 (Dallas, TX); produced by Rambus	2604	5/21/2003	JXA		
CX0100A	Minutes of Interim Meeting JC-42.3 Committee (Sunnyvale, CA); produced by Rambus	2604	5/21/2003	JXA		
CX0110	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 28 (Portland, OR)	5351	6/16/2003	5328:22; 5351:20		
CX0111A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 79 (Portland, OR); produced by Rambus	2604	5/21/2003	JXA		
CX0128	Minutes of Future DRAM Task Group (Mt. View, CA)	4605	6/9/2003	4583:6; 4602:8; 4602:22; 4603:13; 4604:8; 4606:24		
CX0132	Minutes of Future DRAM Task Group (San Jose, CA)	4616	6/9/2003	4614:7; 4615:7; 4616:7; 4688:15		
CX0137	Minutes of DRAM Task Group (San Diego, CA)	4741	6/9/2003	4706:6,11; 4708:8; 4709:12; 4741:1; 4742:3		
CX0140	Minutes of Joint JEDEC/EIAJ Meeting on DRAM Futures (DDRII) Task Group (Tokyo, Japan)	2604	5/21/2003	4709:22		

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CX0150	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 42 (Maui, HI)	2604	5/21/2003	JXA	<u></u>	10.0.0
CX0154A	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 94 (Atlanta, GA)	3135	5/28/2003	2818:12,17; 2819:17; 3134:13; 3135:4		
CX0160	Excerpt from JC-42.5 Committee on Memory Cards and Modules, Meeting Minutes	6071	6/19/2003	6040:24; 6071:8,9		
CX0161	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 96 (Denver, CO) (without attachments)	2604	5/21/2003	JXA		
CX0168	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 98 (San Diego, CA)	4741	6/9/2003	4729:17,18; 4741:17		
CX0171	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 99 (Tokyo, Japan)	3135	5/28/2003	2841:12; 2844:25; 3134:13; 3135:4		
CX0174	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 100 (Las Vegas, NV)	4741	6/9/2003	4736:25; 4741:22		
CX0202	EIA Legal Guides	1854	5/14/2003	1827:4,9,14; 1828:9,16; 1830:2; 1832:3,4,14,16; 1833:13; 1834:6; 1838:21; 1840:2; 1842:10; 1854:14		
CX0202	EIA Legal Guides	2604	5/21/2003	JXA		
CX0203A	Manual for Committee, Subcommittee, and Working Group Chairmen and Secretaries, Engineering Publication: EP-3-F; produced by Rambus	1875	5/14/2003	1859:4; 1862:21; 1865:3,24; 1872:24; 1875:8; 1879:4,6		
CX0203A	Manual for Committee, Subcommittee, and Working Group Chairmen and Secretaries, Engineering Publication: EP-3-F; produced by Rambus	2604	5/21/2003	JXA		
CX0204	EIA Legal Guides (from March 14, 1983)	311	5/1/2003	303:10, 304:13; 304:17,18; 305:6; 311:4; 1328:22; 1459:3; 1460:14; 1825:13; 1827:8,15; 1854:9; 2396:13,19,24; 2398:10; 2682:18; 2946:18; 3485:24; 5207:17,20; 7609:10		
CX0204	EIA Legal Guides (from March 14, 1983)	2604	5/21/2003	JXA		
CX0205	JEDEC Manual of Organization and Procedure JEP21-H	1918	5/14/2003	1913:25; 1915:18; 1916:11; 1917:6,20; 1918:6,17; 1921:14; 1924:22; 2027:20		
CX0205	JEDEC Manual of Organization and Procedure JEP21-H	2604	5/21/2003	JXA		
CX0205A	JEDEC Manual of Organization and Procedure JEP21-H; produced by Rambus	4074	6/3/2003	2216:1; 2684:12,16; 4074:3		
CX0206	Manual of Organization and Procedure: EP-1-J	4074	6/3/2003	4073:23; 4074:3		
CX0207A	Style Manual for Standards and Publications of EIA, TIA and JEDEC: EP-7-A	4073	6/3/2003	2215:20; 4073:13		

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CX0208	JEDEC Manual of Organization and Procedure JEP21-I	326	5/1/2003	309:25; 310:3; 311:12; 312:7; 313:6; 323:2; 326:1; 640:25; 1340:7,15; 1349:3,6,10,21; 1418:25; 1419:3; 1702:22; 1921:2,8; 1923:21; 1924:23; 1925:6,9; 1926:10; 1927:17; 1986:15; 1989:7; 2027:20; 2103:15; 2408:25; 2411:18; 2429:1;3;7; 2431:13; 2666:1,8; 3591:24; 5699:23; 5700:1; 7619:13,22; 10944:19,20; 10945:3; 10964:23		
CX0208	JEDEC Manual of Organization and Procedure JEP21-I	2604	5/21/2003	JXA		
CX0208A	JEDEC Manual of Organization and Procedure JEP21-I; produced by Rambus	2604	5/21/2003			

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CX0303	Presentation by Rhoden re: "Advanced Memory International- An Industry Partnership"	2604	5/21/2003	JXA	iii Camera:	Kelerence
CX0305	Presentation by Rhoden re: "A Vision for SLDRAM [The first order of business should be to change the name!] Inc Lead-Co-ordinate-Teach"	2604	5/21/2003	696:5; 700:11		
CX0306	JEDEC "Meeting Attendance Roster" attaching "General Guides Applicable to All EIA Activities"	345	5/1/2003	342:4; 345:14; 1450:10; 1854:25; 1857:4,9		
CX0306	JEDEC "Meeting Attendance Roster" attaching "General Guides Applicable to All EIA Activities"	2604	5/21/2003	JXA		
CX0314	Presentation by Kalter re: "High Speed TOGGLE for Microprocessor Applications- DRAM Task Group"	2604	5/21/2003	JXA		
CX0315	Presentation by Kalter re: "High Speed TOGGLE for Microprocessor Applications- DRAM Task Group"	2604	5/21/2003	JXA		
CX0316	DRAM Task Group Questionaire by Kelley re TOGGLE Mode	2604	5/21/2003	JXA		
CX0317	Letter from Kinn to Townsend	2604	5/21/2003	JXA		
CX0318	Presentation by Kalter re: "High Speed TOGGLE for Microprocessor Applications- DRAM Task Group"	2604	5/21/2003	JXA		
CX0336	Fax from Townsend to Kelly, Sharp, Tabrizi, Stodier, and others re: "Patent Issues in JEDEC"	976	5/5/2003	904:15; 976:8,13		
CX0340	Sun JEDEC JC-42.3 Presentation re: "Sun Microsystems Proposal for Synchronous DRAM Specification"	5804	6/18/2003	Letter from Townsend to KelMcGheeelly, Shae and others re: "Patent Issues in JEDEC"	er, 976	5/5/20

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CX0352	TI Document re: "Clarification of JC-42.3 Committee's Interpretation of JEDEC Patent Policy"	2498	5/21/2003	2480:18; 2482:24; 2498:11		
CX0353	Memo from Kelly to McGhee re: "ANSI/JEDEC Patent Policy" and attaching TI Document re: "Clarification of JC-42.3 Committee's Interpretation of JEDEC Patent Policy"	1947	5/14/2003	1940:12; 1941:15; 1943:16; 1947:3,13; 1949:22; 1951:23		
CX0353	Memo from Kelly to McGhee re: "ANSI/JEDEC Patent Policy" and attaching TI Document re: "Clarification of JC-42.3 Committee's Interpretation of JEDEC Patent Policy"	2604	5/21/2003	JXA		
CX0355	Letter from McGhee to JC-12 Committee Members re: "EIA Legal Response to TI letter dated March 9, 1994" and attaching Memo from Kelly to McGhee re: "ANSI/JEDEC Patent Policy" and ANSI Manual re: "Guidelines for Implementation of the ANSI Patent Policy- An Aid to More Efficient and Effective Standards Development in Fields That May Involve Patented Technology"	2604	5/21/2003	2482:7,23; 2483:2; 2651:2; 2970:20; 7921:6,7,18		
CX0356	EIA Sign-In Sheet, JC-42 Exec, NYC May 23, 1994, Intercont.	2604	5/21/2003	JXA		
CX0360	Letter from Kilbuck to Kellogg, Leong, Fang, Quddus, and others re: "Action Items from March JC-42.5A Meeting"	2604	5/21/2003	JXA		
CX0361	Email from Rhoden to jc42-mail re: "SDRAM Lite Survey ballot"	518	5/2/2003	515:19; 516:10,14; 518:18		
CX0362	IBM Presentation re: "SDRAM Lite Implications to SDRAM DIMM PDs"	2604	5/21/2003	JXA		
CX0363	Micron Presentation re: "Future SDRAM-Clock Issues- PLL/DLL Circuits and/or Echo Clocks"	2604	5/21/2003	7989:22,24; 7990:4		
CX0364	Letter from Walther to Townsend attaching Article re: "Hitachi Sues SRAM Competitor Winbond" and Handwritten Note	977	5/5/2003	937:7,10; 976:23		
CX0367	VLSI Presentation by Rhoden re: "SLDRAM Vernier Justification"	2604	5/21/2003	JXA		
CX0368	Micron Presentation re: "Proposal for DDR Data Strobe"	2604	5/21/2003	6696:19,24; 6697:8,13		
CX0370	Presentation by Deneroff and Macri re: "We (SGI) believe that Existing DDR proposals DO NOT WORK!- Proposal for DDR SDRAM Clocking"	2604	5/21/2003	4683:17; 4781:10,11,12		

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CX0371	Email from Walther to Ryan, Lee, and Schaefer re: "FW: TI Proposal on DDR Differential Clock" and attaching TI Document re: "Proposal for Full Speed Clock SDRAMs"	6821	6/24/2003	6710:24; 6711:2,12; 6713:17,20; 6821:11; 11038:14; 11040:9; 11088:1		
CX0374	Micron Presentation re: "Future Graphics Memory Direction"	2604	5/21/2003	JXA		
CX0375	Letter from Rhoden to Tabrizi re: "Orlando Meeting"	1202	5/7/2003	1192:9; 1193:17; 1201:3; 1202:16; 1290:3		
CX0376	Email from Macri to akwok, agross, Peng, Zacconi, and others re: "JEDEC: Future dram task group kickoff"	2604	5/21/2003	4593:7; 4594:1		
CX0376A	Email from Macri to akwok, agross, Peng, Zacconi, and others re: "JEDEC: Future dram task group kickoff"	4594	6/9/2003	4589:24; 4590:23; 4593:5		

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CX0529		2604	5/21/2003	JXA	Order	
CX0530		2604	5/21/2003	JXA	In Camera 7/9/2003 Order	
CX0533	Rambus Business Plan, June 26, 1989	2604	5/21/2003	JXA		
CX0534	Rambus Business Plan: Plans, Ideas, Issues	2604	5/21/2003	JXA		
CX0535	Rambus Business Plan	2604	5/21/2003	8302:14		
CX0542	Memo from Tate to "Rambus'ers" re: "Draft of 1992-1994 Business Plan"	8309	7/9/2003	8307:16; 8308:5,13; 8309:4		
CX0543	Letter from Tate to Board Members and others, attaching the "Rambus Inc. 1992-1997 Business Plan dated June/92"	2604	5/21/2003	8319:25; 8320:23; 8321:24; 8322:16; 8328:12; 8331:5; by stipulatoin		
CX0543A	Letter from Tate to Board Members and others, attaching the "Rambus Inc. 1992-1997 Business Plan dated June/92"	2604	5/21/2003	JXA		
CX0543A	Letter from Tate to Board Members and others, attaching the "Rambus Inc. 1992-1997 Business Plan dated June/92"	8209	7/9/2003	3070:20; 3071:1; 8201:20; 8203:7; 8204:20; 8209:16; 10768:22; 10807:18		
CX0545	Rambus Inc. Business Plan 1992-1997, September 1992	2604	5/21/2003	3091:15,21: 3497:4; 8211:22; 8213:10		
CX0547	Rambus Strategic Plan, 1994-1998	2604	5/21/2003	JXA		
CX0548	Rambus Strategic Plan, 1995-1999	2604	5/21/2003	JXA		
CX0550	Document re: "Top Level Key Results for 1998- FINAL 1/98"	2604	5/21/2003	JXA		
CX0551	Rambus Worldwide Strategic Patent Licensing Program, (DRAFT 2/12/98)	2604	5/21/2003	JXA		
CX0569	Rambus document re: "Rambus Business Plan: Plans, Ideas, Issues"	2604	5/21/2003	JXA		
CX0570	Rambus document re: "Rambus Business Plan"	2604	5/21/2003	JXA		
CX0570	Rambus document re: "Rambus Business Plan"	8299	7/9/2003	8294:8; 8295:17; 8299:1; 8450:20		RX0015
CX0601	Application for Membership in Engineering Committees of the JEDEC Solid State Engineering Council; filled out by Billy Garrett of Rambus	3021	5/27/2003	3019:21; 3021:6		
CX0602	Collection of Rambus' JEDEC Membership documents	2604	5/21/2003	3021:16; 3022:3; 3142:20,25; 3143:22; 3176:22; 3177:12; 3249:7		RX0945
CX0604	Minutes of a Regular Meeting of the Board of Directors of Rambus Inc., June 25, 1992	2604	5/21/2003	8209:23; 8211:7; 8319:7; 8320:16		
CX0605	Minutes of a Regular Meeting of the Board of Directors of Rambus Inc., September 17, 1992	2604	5/21/2003	3090:2; 8214:14,15		
CX0606	Minutes of a Regular Meeting of the Board of Directors of Rambus Inc., October 22, 1992	2604	5/21/2003	3105:23; 8216:5; 8218:22; 8340:17; 8368:17; 8596:14; 8597:6		

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CX0682	Email from Crisp to Farmwald re: "lester vincent mtg"	2604	5/21/2003	3111:4; 8337:7		
CX0685	Email from Mooring to Farmwald, Tate, Roberts, Crisp, Mitchell, and Cates re: "JEDEC notes"	2604	5/21/2003	2957:1; 3499:24; 7915:14; 8365:13,15; 8368:10		
CX0686	Email from Crisp to Ware re: "RE patent stuff"	2604	5/21/2003	3119:21; 3120:20; 8341:19		
CX0687	Email from Dillon to Roberts re: "Synchronous High Speed Packet Patent App"	2604	5/21/2003	JXA		
CX0688	Email from Tate to staff re: "Sync DRAMs / High Bandwidth Memories"	2604	5/21/2003	3125:1		
CX0691	Email from Crisp to Ware re: "patent"	2604	5/21/2003	3123:16		
CX0700	Email from Crisp to Tate, Mooring, Mitchell, Rovetrs, Garrett, and Cates re: "jedec notes (thurs)"	2604	5/21/2003	JXA		
CX0702	Email from Ware to Dillon, Roberts, Barth, Crisp, Farmwald re: "patent claim status"	2604	5/21/2003	8342:20; 8347:10; 8371:9		
CX0703	Email from Crisp to Ware re: "Re: patent claim status"	3175	5/28/2003	3156:25; 3157:13; 3165:23; 3166:1		
CX0707	Email from Tate to Mooring, Crisp, Roberts, Farmwald, and Tate re: "dinner 7/21 with Joel Karp"	2604	5/21/2003	JXA		
CX0708	Email from Mooring to Tate, Marketing, Roberts, Farmwald, and Harmon re: "Hyundai Meeting on 9/24/2β508a/5i 3T.76 TD -0.050/8.8 0 TD1M0.0374	Tc (5/21/CX0702)	Tj 41.64 5.28 TD	-0.010906Tc 0.0423 Tw "Moorin'6 Tlink'g, Mie	e m (Eiage of Ramlink	kng, Roberts, 1Tj 2

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CX0718	Email from Roberts to Mooring re: "Patent meeting w/Lester"	2604	5/21/2003	JXA		
CX0724	Email from Crisp to Marketing, Tate, Mako re: "JEDEC day 2 (morning, gen Memories discussion)"	2604	5/21/2003	JXA		
CX0726	Email from Mooring to Roberts and Tate re: "More patents"	2604	5/21/2003	JXA		
CX0728	Email from Tate to Harmon re: "lester v."	2604	5/21/2003	JXA		
CX0730	Email from Roberts to Ware re: "MOST Claims session"	2604	5/21/2003	JXA		
CX0731	Email from Harmon to Tate, Roberts, Mooring re: "U.S. Patents"	2604	5/21/2003	JXA		
CX0733	Email from Tate to Johnson re: "Re: Samsung"	2604	5/21/2003	JXA		
CX0734	Letter from Roberts to Vincent	2604	5/21/2003	2232:23; 3178:17; 3233:3,8,11; 3234:17; 3235:3; 7958:7		
CX0738	Email from Dillon to Exec, Barth, and Ware re: "an overlooked patent claim?"	2604	5/21/2003	3185:12; 3577:20; 7959:19		
CX0740	Email from Tate to Roberts re: "sdram and most patent claims"	2604	5/21/2003	JXA		
CX0742	Email from Crisp to Staff re: "JC16 (high speed signalling, HSTL primarily) & JC42.5 modules' special meeting notes"	2604	5/21/2003	JXA		
CX0745	Letter from Griffin to Roberts, forwarded by Roberts to Barth, Ware and Dillon re: "Preliminary Amendment" to 08/222,646	2604	5/21/2003	7960:18,25; 7961:6; 8352:14; 8353:7		
CX0746	Letter from Griffin to Roberts, forwarded by Roberts to Barth, Ware and Dillon re: "Preliminary Amendment" to 08/222,646	2604	5/21/2003	2233:22; 3195:3; 3198:20; 3578:15		
CX0750	Letter from Roberts to Vincent	3353	5/29/2003	3234:25; 3235:4,7; 3352:21		
CX0751	Email from Ware to Roberts, Dillon, and Barth re: "Lester's New Claim"	2604	5/21/2003	JXA		
CX0753	Email from Harmon to Roberts re: "Re: Foreign Filings"	2604	5/21/2003	JXA		
Cx0755	Email from Ware to Roberts re: "claims"	2604	5/21/2003	JXA		
CX0756	Email from Crisp to Exec and Marketing re: "JEDEC #3 (NEC PROPOSES PLL ON SDRAM!!!)"	2604	5/21/2003	JXA		
CX0757	Email from Crisp to exec re: "patent issues"	2604	5/21/2003	3208:19; 3209:13,18; 3210:5,10,14; 3213:9,11; 3215:9; 3216:1; 3217:3,16; 7967:9; 7968:2		
CX0762	Email from Tate to Exec and Crisp re: "samsung deal details"	2604	5/21/2003	JXA		

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CX0763	Email from Crisp to Roberts and Exec re: "Re: samsung deal details"	2604	5/21/2003	3220:6; 3221:24; 3226:8; 7969:4,6,23		
CX0764	Email from Crisp to Roberts re: "samsung meeting notes from Allen"	2604	5/21/2003	JXA		
CX0765	Email from Crisp to Exec and Crisp re: "Re: samsung deal details - CONCERNS"	2604	5/21/2003	3225:20		
CX0766	Email from Crisp to Tate re: "Re: samsung deal details - CONCERNS"	2604	5/21/2003	3227:9		
CX0767	Email from Harmon to Exec and Crisp re: "Samsung contract"	2604	5/21/2003	JXA		
CX0768	Email from Tate to Staff re: "korea/japan trip 10/21-28 highlights fyi"	2604	5/21/2003	JXA		
CX0770	Email from Harmon to Roberts re: "Re: Samsung"	2604	5/21/2003	JXA		
CX0779	Email from Barth to Roberts, Dillon, and Harmon re: "[roberts: Chat with Tony]	2604	5/21/2003	JXA		
CX0781	Email from Tate to Barth re: "Re: RAmbus-2/UB #2"	2604	5/21/2003	JXA		
CX0782	Email from Crisp to Bdmarket and Exec re: "Hyundai meeting 2/24/95	2604	5/21/2003	3252:24		
CX0783	Email from Crisp to Exec and Bd re: "Farhad Tabrizi/Hyundai/Synclink"	2604	5/21/2003	3255:22; 3257:23; 3258:2; 3259:5; 3533:4; 3534:24		
CX0786	Email from Crisp to Bdmarket and Exec re: "jc42 Meeting (DRAM and SRAM)"TD -645:22; 32-390404 (	).2s e6534:0 Tw 7:2	3; 582; 3259:5;	3533:4;		

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CX0860	Email from Barth to Exec re: "Samsung and synchronous data"	2604	5/21/2003	JXA		
CX0861	Email from Roberts to Exec re: "Sam This morning we met with Samsung. They also were meeting with Intel today"	2604	5/21/2003	JXA		
CX0867	Email from Tate to Diepenbrock re: "ISSCC IP issues"	2604	5/21/2003	JXA		
CX0868	Email from Crisp to Mooring, Tate, Roberts, Toprani, Ware, Chen, and others re: "JEDEC minutes from January interim meeting"	2604	5/21/2003	3363:1; 7989:10,16		
CX0869	Letter from Diepenbrock to Rowden	2604	5/21/2003	6223:11; 6224:3; 10730:1		
CX0871	Email from Diepenbrock to Roberts re: "Re: Thoughts on clocks and DLL"	2604	5/21/2003	6167:2; 6169:10; 6171:21; 6173:23; 6175:3		
CX0873	Letter from Crisp to EIA re: "invoice 28002 (1/10/96)"	2604	5/21/2003	2246:22; 3371:9,13; 3373:3,16; 3381:19,25; 3383:12; 3384:24		
CX0874	Letter from Crisp to EIA re: "invoices 28002 (1/10/96)"	2604	5/21/2003	2246:24; 3371:9,13; 3372:19; 3373:4,16; 3382:10,15; 3383:19,21; 3385:9; 10715:3		
CX0875	Email from Toprani to Tate re: "Re: samsung business resolution"	2604	5/21/2003	JXA		
CX0876	Letter from Crisp to EIA re: "invoice 28002"	2604	5/21/2003	2246:24; 3371:9; 3372:19; 3373:3,16; 3382:24; 3383:23		
CX0879	Letter from Crisp to McGhee re: "JEDEC Invoice No. 28002 Dates January 10, 1996."	2604	5/21/2003	3371:9		
CX0880	Letter from Crisp to EIA re: "invoice 28002 (1/10/96)"	2604	5/21/2003	2246:25; 3371:9; 3372:20; 3373:4,16; 3382:24; 3383:23		
CX0882	Fax from Crisp to Vincent	2604	5/21/2003	3377:3		
CX0883	Draft of Letter from Crisp to EIA re: "JEDEC Invoice No. 28002 dated January 10, 1996"	4075	6/3/2003	2243:20; 2244:18; 4075:20		
CX0886	Email from Barth to Exec re: "LG & 200 MHz SDRAM"	2604	5/21/2003	JXA		
CX0887	Letter from Crisp to McGhee re: "JEDEC Invoice No. 28002 Dated January 10, 1996"	2604	5/21/2003	2247:1; 3380:20,25; 3381:6; 3383:3; 3384:2; 3386:19; 3387:17; 3562:11; 3582:17; 5501:19,25; 5502:4,8; 5645:25		part of CX0602/RX09 45
CX0888	Letter from Crisp to McGhee re: "JEDEC Invoice No. 28002 Dated January 10, 1996"	2604	5/21/2003	6195:10; 6199:6,15; 6200:17		
CX0889	Memo from Diepenbrock to Vincent re: "P001C2 Opinion"	2604	5/21/2003	3379:18; 3570:3; 6195:10,11; 6202:9; 6204:16		
CX0890	Fax from Diepenbrock to Vincent	4076	6/3/2003	2249:22; 4075:25		
CX0891	Fax from Diepenbrock to Mallie	2604	5/21/2003	JXA		
CX0892	Memo from Diepenbrock to Mallie re: "Letter to Mosys"	2604	5/21/2003	JXA		
CX0896	Email from Crisp re: "new synclink spec available"	2604	5/21/2003	JXA		

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CX0936	Email from Tate to Mooring, Fleming, and Roberts re: "BILL'S PROPOSED REPLY TO GERRY - NEED INPUTS ASAP"	2604	5/21/2003	JXA		
CX0937	Email from Mooring to whd@mdv.com re: "Siemens Status"	2604	5/21/2003	JXA		
CX0938	Email from Tate to Fleming and Roberts re: "FW: DRAM Suppliers and Rambus"	2604	5/21/2003	10688:24; 10689:3,19; 10688:16		
CX0939	Email from Davidow to Mooring and Tate re: "FW: DRAM Suppliers and Rambus"	2604	5/21/2003	8409:10,11; 8410:3; 8413:14		
CX0942	Email from Tate to Mooring, Toprani, Roberts, Clarke, Diepenbrock, and others re: "327 patent (method of transferring data on both edges of clock for dram); new patent (p007dc) to issue 8/12 (use of pll/dll for timing compensation on memory interface for memory chip) - 2 action items: prepare response; intel education"	2604	5/21/2003	6177:2; 6180:24; 6183:13		
CX0944	Email from Tate re: "samsung exec updates meeting 8/12/97"	2604	5/21/2003	JXA		
CX0946	Email from Tate to Mooring and Fleming re: "parker/gelsinger ddr crush plan"	2604	5/21/2003	JXA		
CX0947	Email from Clarke re: "Rambus Confidential: Approved Q & A for latest patent"	2604	5/21/2003	3408:25		
CX0948	Email from Clarke to Exec and Bdmarket re: "Rambus Confidential: Approved Q & A for latest patent"	2604	5/21/2003	JXA		
CX0952	Email from Tate to Exec, yfc, Ho, Hampel, Dillon, Diepenbrock, and others re: "intel exec meeting (parker/gelsinger) 9/8/97"	2604	5/21/2003	JXA		
CX0953	Email from Crisp re: "more stuff on the taipei jedec meeting"	2604	5/21/2003	JXA		
CX0954		2604	5/21/2003	JXA	In Camera 5/5/2003 Order	
CX0956	Email from Tate to Engrmgr, Exec, Diepenbrock, Gasbarro, Stark, and others re: "samsung exec meeting seoul 9/24/97"	2604	5/21/2003	JXA		
CX0957	Email from Tate re: "LG/Mr. HJ Chun/Memory GM meeting today 9/25/97 at rambus"	2604	5/21/2003	JXA		
CX0960	Email from Tate to Exec re: "joel karp accepted; keep quiet till he starts"	2604	5/21/2003	JXA		
CX0961	Email from Tate re: "1:1 with pat gelsinger (and pete macwilliams) in portland 10/13/97	2604	5/21/2003	3168:5		

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CX1007	Email from Mooring to Exec and Fleming re:  "Proposed Amendments to contract"	2604	5/21/2003	JXA
CX1014	Email from Crisp re: "anti Rambus/Intel seminar info. other G2 regarding JEDEC etc."	2604	5/21/2003	JXA
CX1016	Email from Tate to Exec, Fleming, Dunlevie, Horowitz, and others re: "pat gelsinger/intel meeting 4/14/96 **NOTE:RESTRICTED DISTRIBUTION**	2604	5/21/2003	8227:24; 8228:17; 8401:21; 8402:14; 8403:12; 8404:9,12; 8405:7
CX1017	Email from Tate to Karp, with embedded email from Donnelly	2604	5/21/2003	JXA
CX1020	Email from Crisp re: "package of SDRAM, comments"	260.4 foEmnaiowT00	/1Mooriinge( 48917Ra	JXA Danus/Inn Danvadow 02 j22 93; TimeT: ngger 41.664; 23 llevia2 5 Tic 1/C XT 1ct 00 . 6 Tww (12/246) 00 ) 4 ) TT j2 6/3 . 02/4 10
CX1021	CX1t Tc / Pag*			

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CX1069	Email from Karp re: "Industry group will push DDR DRAMs"	2604	5/21/2003	JXA		
CX1074	Email from Mooring to Karp re: "Re: What Hyundai said at Bay Networks"	2604	5/21/2003	JXA		
CX1075	Email from Karp re: "Here's the interview by Marcy"	2604	5/21/2003	JXA		
CX1077	Email from Karp re: "street.com Intel to Back Alternative to Rambus Chips"	2604	5/21/2003	JXA		
CX1079	Email from Crisp re: "original DDR datasheet anyone?"	2604	5/21/2003	3430:23		
CX1080		2604	5/21/2003	JXA	In Camera 5/5/2003 Order	
CX1083	Email from Tate re: "recap of promotions announced at staff meeting 11/18/99"	2604	5/21/2003	JXA		
CX1084	Email from Crisp re: "ddr meaning"	2604	5/21/2003	3434:11		
CX1085	Email from karp re: "New Patents"	2604	5/21/2003	JXA		
CX1089	Email from Tate re: "question on IP w.r.I DDR"	2604	5/21/2003	10787:15; 10789:17		
CX1097	Email from Tate to Exec, Whd, Dunlevie, Geschke, Horowitz, and Mooring, and others re: "Meeting tonight with otellini/swope 1/18/00"	2604	5/21/2003	JXA		
CX1099		2604	5/21/2003	JXA	In Camera 5/5/2003 Order	
CX1099A	Redacted (public) version of CX1099	11385	8/1/2003	11184:10; 11384:6,14; 11385:11		
CX1104	Email from Toprani to Mooring re: "Re: Mosys"	2604	5/21/2003	JXA		
CX1109	Letter from Steinberg to Nagasawa	3461	5/29/2003	3435:24; 3436:14; 3460:25		
CX1115	, , ,	2604	5/21/2003	JXA	In Camera 5/5/2003 Order	
CX1121	Email from Chen re: "Embedded Microprocessor Forum Conference"	2604	5/21/2003	JXA		
CX1127	Email from Steinberg to Zitzewitz	2604	5/21/2003	JXA		
CX1129	Letter from Steinberg to Park	3461	5/29/2003	3436:1,7,14; 3461:5		
CX1132	Email from Smith re: "MDR/Glskowsky on Lexington & Conexant buys HotRail"	2604	5/21/2003	JXA		
CX1137	Letter from Tate to Eggers	2604	5/21/2003	JXA		
CX1140	Email from Tate to Appleton re: "Re: your email and initiation of litigation"	6404	6/20/2003	6393:8,18; 6394:10; 6395:25; 6403:25; 6412:13,22; 6419:20		
CX1141		2604	5/21/2003	JXA		
CX1141A	Redacted (public) version of CX1141	11385	8/1/2003	11184:11; 11384:15; 11385:11		
CX1146	Email Tate to ssywlee@samsung.co.kr re: "SDR/DDR Patent License Agreement"	2604	5/21/2003	JXA		

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CX1149		11404	8/1/2003	11393:4; 11404:3,8	In CameraJoint Stipulation filed with the Court on September 29, 2003 - order pending	
CX1150		11404	8/1/2003	11393:4; 11404:3,8	In CameraJoint Stipulation filed with the Court on September 29, 2003 - order pending	

CX1154 Email from Tate to Staff re: "great job on

samsung/lexington!HTj 2Cameramr71 TD 0 0s8AB72 rp.48 51f5g0!i9 TD -0.0207 Tc 0.0521 Tw (- order pending) Tj -572.16 -21 TD /F1 8.52 Tf -0.0505 Tc 0 Tw (CX1154) Tj 41.64

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CX1264	Rambus presentation: "EMAIL-THROW IT AWAY"	2604	5/21/2003	3421:1,6; 3570:15; 6230:23; 7792:15		
CX1267	Rambus presentation by Diepenbrock: "IP Strategy"	2604	5/21/2003	6129:6,8		
CX1268	Infineon presentation: "Semiconductor Memory Device"	2604	5/21/2003	JXA		
CX1269	Presentation: "Semiconductor Memory Device"	2604	5/21/2003	JXA		
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	Rambus presentation: "200MHz Myth?"	2604	5/21/2003	JXA		
CX1282	Rambus presentation by Mannos, Farmwald, and Horowitz: "RamBus Business Overview"	2604	5/21/2003	8299:11; 8300:3		RX0019
CX1283	Rambus Presentation by Farmwald and Horowitz: "RamBus"	8285	7/9/2003	8278:17; 8279:8; 8280:6; 8282:14; 8283:14; 8285:12; 8290:14; 8452:24; 8453:5; 8456:15; 8458:24; 8460:7,15,19; 8461:21		RX0082
CX1284	Rambus presentation by Farmwald: "RamBus Technology Overview"	2604	5/21/2003	JXA		RX0021
CX1287	Rambus presentation by Farmwald and Horowitz: "RamBus Technology Overview"	2604	5/21/2003	JXA		
CX1291	Rambus presentation: "Rambus Inc. Executive Summary March 1991"	2604	5/21/2003	JXA		
CX1302	Rambus presentation: "Rambus Inc. Business Plan 1992-1997"	2604	5/21/2003	8318:3; 8329:8		
CX1304	Rambus presentation: "Hitachi/Rambus Update 12/92"	2604	5/21/2003	JXA		
CX1309	Rambus presentation: "Rambus Technology Overview"	2604	5/21/2003	9635:14,21,24; 9637:12		
CX1312	Rambus presentation: "Outline"	2604	5/21/2003	JXA		
CX1314	Rambus presentation: "Micron December 14, 1995"	7097	6/25/2003	6837:16; 7043:17; 7097:15		
CX1316	Rambus presentation: "Mosys Competitive Summary"	2604	5/21/2003	3370:5,9		
CX1317	Rambus presentation: "Rambus /Morgan Stanley February 1996 Rambus Confidential Information"	2604	5/21/2003	JXA		
CX1319	Rambus presentation: "Mosys Competitive Summary"	2604	5/21/2003	3370:19		
CX1320	Rambus presentation: "Why SDRAMs won't run fast in PCs"	2604	5/21/2003	2922:20; 2923:7; 2925:12; 3392:19,25; 3393:15; 3394:19		
CX1322	Rambus presentation by Horowitz: "Merged DRAM/Logic"	8761	7/15/2003	8573:23; 8583:18		

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CX1340	Rambus presentation by Tate: "Semiconductor Intellectual Property: New Market New Model New Strategy"	2604	5/21/2003	JXA		
CX1343	Rambus presentation: "Rambus Big Picture Update Chaminade November 1998"	2604	5/21/2003	JXA		
CX1353	Rambus presentation: "Promotions 11/18/99 Rambus Confidential"	2604	5/21/2003	JXA		
CX1354	Rambus presentation by Tate: "Rambus Business Model & DRAM Industry Dynamics"	2604	5/21/2003	JXA		
CX1355	Rambus presentation: "Status, Plans, and Operations Review Rambus/Intel Meeting December 3, 1999"	8702	7/14/2003	8678:12; 8702:5		
CX1355A	Rambus presentation: "Status, Plans, and Operations Review Rambus/Intel Meeting December 3, 1999"	8702	7/14/2003	8701:23		
CX1359		2604	5/21/2003	JXA	In Camera 5/5/2003 Order	
CX1363	Rambus presentation: "Rambus Update Jan 2000'	2604	5/21/2003	JXA	21.22	
CX1364		11404	8/1/2003	11184:25; 11393:4; 11404:3,8	In CameraJoint Stipulation filed with the Court on September 29, 2003 - order pending	
CX1366	Rambus presentation by Tate and Harmon: "Rambus Inc. February 2000"	2604	5/21/2003	JXA		
CX1368	Rambus presentation: "WW12 Micron Meeting March 13, 2000"	8712	7/14/2003	8708:12; 8712:4		
CX1371	Rambus presentation: "Rambus-NVIDIA Patent License Negotiations April 5, 2000"	2604	5/21/2003	JXA		
CX1379	Rambus presentation: "Intel Executive Meeting August 8, 2000"	2604	5/21/2003	JXA		
CX1379	Rambus presentation: "Intel Executive Meeting August 8, 2000"	8430	7/10/2003	8425:6Jh2Tj 233Tj A9233T30:21j -390.6 -21 August 8, 2000"	.36 48Tj A9233T30:21j -	390.6 -21.36 40.6

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CX1384		4756	6/9/2003	4750:19; 4753:9; 4756:15	In Camera 5/5/2003 Order	
CV1205	Rambus presentation: "Analyst Meetiri TD -0.0257	Tc 0g614.88 510 TI	D -5 Tw7.ambus pre	esentation: "Analyst Me		

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CX1451	Rambus Patent Application 07/510,898	1525	5/12/2003	1514:5; 1525:1,14; 2915:10,25; 2916:13; 2919:1; 5462:8,16,20,24; 5467:3,4; 5470:1,6,10; 5472:21; 5473:7; 5474:3,5; 5476:25; 5477:6; 5478:19; 5479:2; 5480:19; 5481:5,20; 5485:11,20; 5486:25; 5489:25; 5490:17,23; 5496:19; 5648:6,12; 8249:22; 8250:20; 8255:9; 8256:15; 8259:13; 8261:3; 8263:18; 8266:12; 8614:13; 8620:18; 8625:4; 8634:3; 8638:10; 8643:4,11; 8644:8; 8647:20; 8649:1; 8657:12		
CX1451	Rambus Patent Application 07/510,898	2604	5/21/2003	JXA		
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CX1453	Rambus' Republic of China Patent NI-48411	2604	5/21/2003	JXA		
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CX1456	Prosecution History of Rambus Patent Application 07/847,651	2604	5/21/2003	JXA		
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CX1458	Rambus patent document re: "Preliminary Amendment" to the 07,847,651 Patent Application	2604	5/21/2003	3150:24; 3151:16; 7950:11,13; 7951:6; 7953:24; 7954:4,10,11		
CX1459	Rambus patent document re: "Preliminary Amendment" to the 07,847,692 Patent Application	2604	5/21/2003	2225:2; 2242:8; 3167:6; 5710:4; 6158:25; 6159:1; 7955:22		
CX1460	Rambus Patent 5,243,703	2604	5/21/2003	5498:15,19; 5499:11; 6609:2,7,11; 6830:10		
CX1461	Rambus Patent 5,254,883	2604	5/21/2003	JXA		
CX1462	Rambus Patent 5,268,639	2604	5/21/2003	JXA		
CX1463	Rambus Patent 5,319,755	2604	5/21/2003	JXA		
CX1464	Rambus Patent 5,325,053	2604	5/21/2003	JXA		
CX1465	Rambus Patent 5,337,285	2604	5/21/2003	JXA		
CX1466	Rambus patent document re: "Preliminary Amendment" to the 08/222,646 Patent Application	2604	5/21/2003	7964:10,12		
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CX1469	Rambus Patent 5,422,529	2604	5/21/2003	JXA		

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CX1470	Rambus patent document re: "Amendment" to the	2604	5/21/2003	3235:17; 3236:18; 3237:20; 3238:13;		
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	.IXA 1971.14JAA 3233.17, 3230.	10, 3237.20, 3230.13	5,			

Pa99,3510.68 TD -0.247ET 385.56 519.12 29.64 056 519.12 29.64 0.057TD -0.0086 Tc35 Tc 0.0671:14

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CX1497	Rambus patent document re: "Amendment After	2604	5/21/2003	JXA		
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CX1503	Rambus Patent 5,657,481	2604	5/21/2003	3407:16		
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CX1505	Prosecution History of Rambus Patent Application 08/979,127	2604	5/21/2003	JXA		
CX1506	Rambus' Republic of China Patent NI-092069	2604	5/21/2003	JXA		
CX1507	Prosecution History of Rambus Patent 5,953,263	2604	5/21/2003	JXA		
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CX1509	Prosecution History of Rambus Patent 5,841,580	2604	5/21/2003	JXA		
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CX1511	Rambus Korean Patent 179666	2604	5/21/2003	JXA		
CX1512	Prosecution History of Rambus Patent 6,034,918	2604	5/21/2003	JXA		
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CX1518	Rambus Patent 5,954,804	2604	5/21/2003	JXA		
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CX1530	Rambus Paten 6,067,592	2604	5/21/2003			
CX1531	Prosecution History of Rambus Patent 6,038,195	2604	5/21/2003	JXA		
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CX1534	Prosecution History of Rambus Patent Application 09/796,206	2604	5/21/2003	JXA		
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CX1537	Prosecution History of Rambus Patent 6,324,120	2604	5/21/2003	JXA		
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CX1542	Prosecution History of Rambus Patent Application 10/028,077	2604	5/21/2003	JXA		
CX1543	Rambus Patent 6,426,916	2604	5/21/2003	JXA		
CX1544	Rambus Patent 6,452,863	2604	5/21/2003	JXA		
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CX1599	"Semiconductor Technology License Agreement Between Hyundai Electronics Industries Co. Ltd. and Rambus Inc."	3353	5/29/2003	3320:16; 3353:8		
CX1600	"Semiconductor Technology License Agreement Between Hyundai Electronics Industries Co. Ltd. and Rambus Inc."	2604	5/21/2003	JXA		
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CX1612	"Amendment No. 1 to Semiconductor Technology Agreement" between Hyundai and Rambus	4110	6/3/2003	4110:1		
CX1617	"Direct Rambus Interface Technology License Agreement" between Rambus and Siemens	2604	5/21/2003	JXA		
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CX1683		2604	5/21/2003	JXA	In Camera 5/5/2003	
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CX1725	Roberts' Handwritten Notes	2604	5/21/2003	JXA		
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CX1727A	Redacted (public) version of CX1727	11385	8/1/2003	8385:5,25; 8389:9; 11184:19; 11384:25;		
OKITZIK	Treducted (public) version of extr27	11000	0/1/2000	11385:13		
CX1729		2604	5/21/2003	JXA	In Camera 5/5/2003	
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CX1808	Brochure re: "Litigation Risk Management- Protecting your technology assets"	2604	5/21/2003	JXA		
CX1821	Document re: "Architectural Issues- Revision 0.94"	2604	5/21/2003	3115:12; 8525:17		
CX1840	Rambus Document re: "Base RDRAM Design Guide"	2604	5/21/2003	JXA		
CX1844	Article by Crisp re: "Direct Rambus Technology: The New Main Memory Standard"	2604	5/21/2003	JXA		
CX1845	Rambus Document re: "Direct Rambus Technical Description Version 0.6.1"	2604	5/21/2003	JXA		
CX1849	Rambus Document re: "RIMM Module Specification 0.3"	2604	5/21/2003	JXA		
CX1855	Pleading re: "Rambus Inc v. Hitachi, Ltd Complaint for Patent Infringement"	2604	5/21/2003	JXA		
CX1859	Pleading re: "In the Matter of Certain Synchronous Dynamic Random Access Memory Devices, Microprocessors, and Products Containing Same- Complaint Under Section 337 of the Tariff Act of 1930 as Amended"	2604	5/21/2003	5)24/20n therm -070the30.05PoN-Rambus Tr	nd Pn "	

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CX1970	Vincent Handwritten Notes from "Conf" with Harmon, Tate and Roberts	2604	5/21/2003	2229:2; 7957:12	in Camera:	Kelelelice
CX1971	Excepts from Blakely Sokoloff Bills to Rambus Inc.	2604	5/21/2003	2229:3		
CX1972	Letter from Vincent to Harmon, cc: Roberts	2604	5/21/2003	JXA		
CX1973	Collection of Blakely Sokoloff Billing Records	4075	6/3/2003	2236:17,19; 4075:3		
CX1974	Letter from Vincent to Roberts	4074	6/3/2003	4074:13		
CX1976	IEEE publication "Draft Standard for a High-Speed Memory Interface (SyncLink)	4074	6/3/2003	2235:20,21; 4074:13		
CX1977	Excepts from Blakely Sokoloff Bills to Rambus Inc.	4072	6/3/2003	4072:3		
CX1978	Sobrino Handwritten Notes from discussion with Barth	2604	5/21/2003	2234:22,24		
CX1982	Collection of Rambus documents re: foreign patent filings	7901	6/30/2003	7880:22; 7900:25		
CX1983	Vincent Handwritten Notes from "Teleconf" with Barth	4075	6/3/2003	2236:10; 4074:23		
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CX1989	Document from Goates re: "FTC investigates Dell"	2604	5/21/2003	2239:16; 2240:22		
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CX2085	Meyer Infineon Deposition (4/21/01)	2899	5/22/2003	Read-in portions admitted	<u> </u>	<u>IXOIOI OI IOO</u>
CX2086	Crisp Micron Deposition (4/23/01)	n/a	pending	Designations submitted 8/25/03		
CX2087	Crisp Micron Deposition (4/24/01)	n/a	pending	Designations submitted 8/25/03		
CX2088	Tate Infineon Trial Testimony (4/25/01)	n/a	pending	Designations submitted 8/25/03		
CX2088	Meyer Infineon Trial Testimony (4/25/01)	2871	5/22/2003	Read-in portions admitted		
CX2089	Meyer Infineon Trial Testimony (4/26/01)	2877	5/22/2003	Read-in portions admitted		
CX2092	Crisp Infineon Trial Testimony (5/02/01)	n/a	pending	Designations submitted 8/25/03		
CX2093	Crisp Infineon Trial Testimony (5/03/01)	n/a	pending	Designations submitted 8/25/03		
CX2094	Crisp Micron Deposition (7/20/01)	n/a	pending	Designations submitted 8/25/03		
CX2096	Farmwald Micron Deposition (7/25/01)	n/a	pending	Designations submitted 8/25/03		
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CX2105	Mooring Hynix Deposition (10/24/01)	n/a	pending	Designations submitted 8/25/03		
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CX2110	Brown FTC Deposition (1/22/03)	10847	7/28/2003	Read-in portions admitted		
CX2111	Tate FTC Deposition (1/22/03)	n/a	pending	Designations submitted 8/25/03		
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CX2115	Ware FTC Deposition (1/13/03)	n/a	pending	Designations submitted 8/25/03		
CX2116	Horowitz FTC Deposition (2/27/03)	n/a	pending	Designations submitted 8/25/03		
CX2152	Document re: ""K7 Systems Overflow #5""	3987	6/2/2003	3983:16; 3987:11; 3996:7; 4005:25		
CX2153	Email from Polzin to Hsu re: "Re: About DDR"	3948	6/2/2003	3944:14; 3948:1		
CX2158	Email from Polzin to Meyer and Murray re: "DDR and Rambus History"	3818	5/30/2003	3796:14,23; 3797:3; 3818:16; 3950:8,14,21; 4011:10		
CX2164	AMD Annual Report	3818	5/30/2003	3759:25; 3760:8; 3818:15		
CX2180	Dell Presentation re: ""RDRAM Outlook""	2604	5/21/2003	JXA		RX2162
CX2191	Memo from Furnweger to Higuchi and Nagami re: "Minutes for September 1993 JEDEC meetings"	2604	5/21/2003	JXA		
CX2192	Memo from Furnweger to Higuchi and Nagami re: "Minutes for December JEDEC meetings"	2604	5/21/2003	JXA		
CX2219	Apple Document re: "Comparison of SyncLink alternatives"	2604	5/21/2003	JXA		

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EXHIBIT #	Article re: "Siemens to deliver high-speed SyncLink	<u>iviai keu</u>	<u>Marked</u>	<u>Mentioned/JX#/Notes</u>	in Camera?	<u>Reference</u>
CX2222	chip early nest year SLDRAM offered as alternative to Rambus' 'Direct'"	2604	5/21/2003	JXA		
CX2225	Gartner Report re: "DRAM Supply and Demand Quarterly Statistics"	2604	5/21/2003	JXA		
CX2237	Document by Martinez re: "JEDEC Committee Executive Summary, Meeting Reports, Meeting Minutes, & Handouts Crystal City, VA September 11- 14, 1995"	4109	6/3/2003	JXA		
CX2241	Document from Martinez to Tabrizi re: "JC 42.3C Meeting, JEDEC Graphics/VRAM Committee Meeting Report, March 20, 1996" and attaching Presentation re: "SGRAM Next Generation"	2604	5/21/2003	JXA		
CX2250	Email from Lee to Cloud re: "Supplier comments" Folder re: "JC42.3C-RAM Function/Feature/SD" and	9110	7/16/2003	9047:23; 9110:16		
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CX2263	Email from Jung to Tabnak re: "IBM(Nov.6) and HP(Nov.13) Meeting Summary"	2604	5/21/2003	9157:5,8		
CX2264	Email from Park to Ho, Oh, Sangjin, and Jung re: "IBM T&Q Meeting summary on Nov.06 in Korea"	2604	5/21/2003	9159:1		
CX2273	Folder re: "Executive Summary" and containing Document from Memory Strategic Marketing to Jeon, Han, Kim, Shin, and others re: "Executive Summary of the JEDEC Meeting on December 8-12, 1997"	2604	5/21/2003	JXA		
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CX2383	Letter from Lynch to JEDEC Member	5979	6/18/2003	5791:15; 5798:5; 5922:1,20; 5923:5		
CX2384	Letter from Kelley to Slay re: "JEDEC Council Ballot JCB-93-75"	2604	5/21/2003	2474:3		
CX2385	Email from Kellogg to Hopp, Walsh, and Hovis re: "(U) SDRAM table"	2604	5/21/2003	JXA		
CX2387	Email from Kelley to Kelley re: "256Mbit SyncDRAMs"	2604	5/21/2003	2587:17		
CX2403	Document re: "512 MBit Synchronous DRAM"	4537	6/6/2003	4389:7; 4398:3; 4496:11; 4499:16; 4537:7		
CX2404	Infineon datasheet re: "128-MBit Synchronous DRAM"	4537	6/6/2003	4389:7; 4398:3; 4496:11; 4499:9,15; 4537:7		
CX2405	Infineon datasheet re: "256 MBit Synchronous DRAM"	5979	6/18/2003	5941:12,16; 5979:14		
CX2408	Infineon datasheet re: "256-MBit Double Data Rate SDRAM"	4537	6/6/2003	4389:22; 4390:5,10,17; 4391:3,11,24; 4392:8,21,25; 4393:8,17,19; 4394:19; 4395:1,11,13,21; 4396:13; 4397:12,16; 4496:12; 4499:16; 4537:7		
CX2410	Infineon datasheet re: "512-MBit Double Data Rate SDRAM "	4537	6/6/2003	4389:14; 4398:11; 4496:11; 4497:10,25; 4498:19; 4499:16; 4537:7		
CX2419	Document by Meyer re: "JEDEC MOS-MEMORY- MEETING"	2604	5/21/2003	JXA		
CX2425	Presentation re: "Siemens Worldwide DRAM Market Model"	2604	5/21/2003	JXA		
CX2427	Siemens Presentation re: "DRAM Market Perspectives"	4536	6/6/2003	4496:1; 4536:18		
CX2428	Infineon Presentation re: "Roadmap: RDRAM"	4537	6/6/2003	4419:9; 4422:13; 4537:17		
CX2431	Document re: "Siemens Toggle- 16Mbit-High Bandwidth DRAM"	2604	5/21/2003	JXA		
CX2435	Siemens Presentation re: "SDRAM II Rambus II SLDRAM"	2604	5/21/2003	JXA		
CX2438 CX2441	Email from usib4bei to Russell re: "256M 0.18"  Presentation re: "PC Market"	2604 2604	5/21/2003 5/21/2003	JXA JXA		
CX2442	Document re: "General Comment" and attaching Presentation re: "Worldwide DRAM Market-Siemens Excellence in DRAM"	2604	5/21/2003	JXA		
CX2444	German Document attaching Document re: "CeBIT 98-Highlights"	2604	5/21/2003	JXA		

CX2449

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Exhibit #	Description	Page Admitted/ Marked	Date Admitted/ Marked	<u>Transcript Page Where Exhibit is</u> Mentioned/JX#/Notes	In Camera?	Cross- Reference
CX2455	Infineon Presentation re: "Infineon Product Roadmap-SDRAM"	4538	6/6/2003	4435:5,13,17; 4437:19,23; 4538:2		
CX2457	Infineon Presentation by Ho, Zilinskas, and Hartner re: "Chipset Driver Roadmap & Direction 2/21/00"	4537	6/6/2003	4431:25; 4432:6; 4537:22		
CX2459	Email from Meyer to Montag re: "JEDEC Highlights 06-09 March 2000, Atlanta (travel report)"	4538	6/6/2003	4441:23; 4538:8		RX1613
CX2462	Infineon Presentation re: "Technology Roadmaps- Memory Product Marketing"	2604	5/21/2003	JXA		
CX2463	Infineon Presentation by Eckelmann re: "New Architectures Introducing Roadmap"	4536	6/6/2003	4489:6; 4536:18		
CX2464	Infineon Presentation re: "Worldwide DRAM Revenue Ranking 2001"	2604	5/21/2003	JXA		
CX2466	Infineon Brochure	1140	5/7/2003	1140:5,6,16; 1146:1; 1175:3		
CX2501		5073	6/12/2003	5070:2; 5073:10; 5079:24	In Camera 4/23/2003 Order	
CX2503		5078	6/12/2003	5073:17,20; 5074:12; 5077:14; 5078:1,20	In Camera 4/23/2003 Order	
CX2521	Letter from Lee to Gelsinger	4880	6/12/2003	4874:20; 4878:7; 4880:20		
CX2522	Email from Calder to Gelsinger, Nachtsheim, MacWilliams, Miller, and others re: "Final Micron Materials" and attaching Document by Calder re: "Intel Investment in Micron Q&A and Positioning"	4981	6/12/2003	4954:9,15; 4946:3; 4980:25; 4981:2		
CX2527	Email from Swope to Otellini and others re: "Rambus review minutes" with attachment	4864	6/12/2003	4860:3; 4864:2		
CX2529	Email from Miner to Close and others re: "server memory update"	4885	6/12/2003	4882:20; 4885:17		
CX2535	Email from Ahmad to MacWilliams re: "Backwards Compat"	4892	6/12/2003	4885:25; 4890:5; 4892:9; 4924:19,20; 4928:23		
CX2536	Email from Ahmad to MacWilliams, Gelsinger re: "FW: Backwards Compat"	4890	6/12/2003	4885:25; 4886:1; 4889:16		
CX2537	Email from Ahmad to Gelsinger, Swope, MacWilliams re: "Slides for my trip: Rambus foils for this weeks trip"	4894	6/12/2003	4893:11; 4894:16		
CX2540	Email from Rausch to MacWilliams re: "PCG DDR task group kickoff mtg preparation, kickoff 10/15/99"	4897	6/12/2003	4894:23; 4896:24		
CX2541	Letter from Gelsinger at Intel to Tate and Davidow re: Intel/Rambus	4902	6/12/2003	4896:23; 4902:7; 4948:25; 4949:17; 8418:12; 8419:15; 8420:25; 8422:2; 8424:10		

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Exhibit #	<u>Description</u>	<u>Marked</u>	<u>Marked</u>		In Camera?	Reference
CX2541	Letter from Gelsinger at Intel to Tate and Davidow re: Intel/Rambus	8424	7/10/2003	4896:23; 4902:7; 4948:25; 4949:17; 8418:12;		
CX2542	Letter from Gelsinger at Intel to Tate and Davidow	8436	7/10/2003	8434:3; 8436:19		
CX2546	Letter from Gelsinger at Intel to Tate and Davidow	8437	7/10/2003	8434:20; 8436:19		
CX2558	Email from Ahmad to Krisa re: "RE: DDR IP?"	8032	6/30/2003	7742:3; 7783:11; 8032:9		
CX2559	Email from Burns to MacWilliams re: "RE: Rambus and Hitachi Settle legal dispute"	4906	6/12/2003	4902:14; 4906:15; 1950:25; 4952:8		
CX2560	Email from MacWilliams to Burns re: "RE: Minutes from OEM Roadmap-Communication Review w/ Jeff/Tony"	4915	6/12/2003	4911:20; 4915:7		
CX2561	Email from Ahmad to Albert re: "RE: ASIA DRAM Supplier Visit Summary"	8032	6/30/2003	7750:15; 7783:11; 8032:15		
CX2606	Memo from Karabatsos re: "1) OUTLINE OF KENTRON TECHNOLOGIES PATENT PENDING CLAIMS FOR THE LOW PROFILE DIMM 2) PROPOSAL FOR PROVIDING KENTRON TECHNOLOGIES DESIGN TO JEDEC MEMBERS"	6070	6/19/2003	6063:24; 6070:16		
CX2608	Letter from Goodman to Kelly	2003	5/14/2003	1996:17,24; 1998:11; 2000:21; 2003:8; 6056:15		RX1748
CX2610	Letter from Kelly to Goodman	2003	5/14/2003	2001:24; 2003:8; 6061:4; 6069:4		
CX2611	Kentron Patent 6,446,158 B1	6095	6/19/2003	6088:16,22: 6095:12		
CX2613	Kentron presentation: "QBM QUAD BAND MEMORY (QBM): Doubling DDR Bandwidth Performance"	6071	6/19/2003	6042:4; 6053:4; 6055:14; 6083:8; 6086:18: 9716:24		
CX2626	Article re: "A Next Generation Channeled-DRAM Architecture with Direct Background Operation and Delayed Channel-Replacement Technique"	2604	5/21/2003	JXA		
CX2630	Article re: "DRAMs For New Memory System (Part 2)"	2604	5/21/2003	JXA		
CX2631	Article re: "What is Burst EDO?"	2604	5/21/2003	JXA		
CX2632	Article re: "The Burst EDO DRAM Advantage"	838/977	5/5/2003	827:22; 828:9; 838:14; 872:25; 874:24; 977:3		
CX2632	Article re: "The Burst EDO DRAM Advantage"	2604	5/21/2003	827:22; 828:9; 838:14; 972:25; 974:24; 977:3		
CX2633	Article re: "SyncLink rallies against Rambus"	2604	5/21/2003	JXA		
CX2634	Article re: "Intel's RDRAM Strategy a Sure Winner"	2604	5/21/2003	JXA		
CX2636	Article re: "DDR SDRAM Functionality and Controller Read Data Capture"	2604	5/21/2003	872:25		
CX2662	Excerpt from Meeting Minutes	2604	5/21/2003	JXA		

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CX2666	Fax from Williams to Lin and attaching "Minutes of Meeting No. 74 JC-42.3 RAM Memories March 15, 1995 Las Vegas, NV"	2604	5/21/2003	JXA		RX0585
CX2675	Enhanced Memory Systems Inc. Presentation re: "EDDR SDRAM Presentation"	2604	5/21/2003	JXA		
CX2676	Presentation re: "SDRAM Mode Register CL settings"	2604	5/21/2003	JXA		
CX2677	Presentation re: "DDR II Modified Write Burst Length- An End to Data Mask"	2604	5/21/2003	JXA		
CX2683	Micron Document re: "Burst EDO DRAMs take performance to new heights"	2604	5/21/2003	JXA		
CX2684	Fax from Williams to Rimpo attaching Document from Bittick to Appleton, Grant, Baldwin, Cloud, and others re: "Finalized Burst EDO Press Release"	2604	5/21/2003	JXA		
CX2690	Fax from Williams to Eckelmann attaching diagrams	2604	5/21/2003	JXA		
CX2691		2604	5/21/2003	JXA	In Camera 4/29/2003 Order	
CX2699		2604	5/21/2003	JXA	In Camera 4/29/2003 Order	
CX2703	S3 Presentation by Gervasi, Yang et al, titled "DDR Technical Analysis"	2604	5/21/2003	JXA		
CX2704	Samsung Presentation, titled "DDR Market Feedback"	2604	5/21/2003	JXA		
CX2705	Micron Presentation, titled "Proposal for Improving DDR Acceptance"	2604	5/21/2003	JXA		
CX2712	Email from Tabrizi to Lee re: "Presentation" with attachment	2604	5/21/2003	JXA		
CX2713	Email from Walther to Ryan, Schaefer and others re: "message from Micron" with embedded Email	2604	5/21/2003	6651:7		
CX2716	Email from Lee to Basile, cc: Lee re: "Estimates"	2604	5/21/2003	JXA		RX1094
CX2717	Transmeta Presentation by Gervasi, titled "JEDEC Memory Future Issues"	2604	5/21/2003	6770:2,16		
CX2718	Micron Presentation, titled "Micron DRAM Update"	6821	6/24/2003	6719:9,12; 6723:5; 6724:15; 6743:25; 6821:16		
CX2720	Email from Tabrizi to Gustafson and Lee re: "SLDRAM presentation" with attachment	2604	5/21/2003	JXA		
CX2722	Email from Ryan to Naghshineh, cc: Tabrizi and others re: "sldram core" with embedded Email	2604	5/21/2003	9223:23		
CX2723	Email from Walther to Ryan, Lee and Schaefer re: "Future DRAM user survey" with embedded Email	2604	5/21/2003	JXA		

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	Email from Ryan to Schulz, cc: Fang re: "2nd					
CX2754	Presentation for December Meeting" with embedded	2604	5/21/2003	JXA		
	Emails					
CX2756	Email from Ryan to Morzano, Lee and wli re: "Write	2604	5/21/2003	JXA		
	Masking" with attached presentation					
CX2757	Email from Ryan to Walther, cc: Lee re: "JEDEC e-	2604	5/21/2003	JXA		
	ballot Committee Ballot JC-42.3-00-39"  Micron Presentation Re: Simplifying Read Latency					
CX2758	for DDR II"	2604	5/21/2003	6777:23; 6779:8; 6782:25; 6785:9; 6788:7		
	Micron Presentation re: "Pin Selectable Posted CAS					
CX2766	for DDR II"	2604	5/21/2003	4717:1; 6787:13		
	Email from Walther to Mailloux, Skidmore, Johnson					
CX2767	and others re: "Tom's Hardware Guide has a few	2604	5/21/2003	JXA		
0,12,0,	questions on recent developments"	2001	0/21/2000			
0)/0700	Micron Presentation, titled "Considerations for DDR-II	1711	0/0/0000	1740 40 4744 40 0705 05 0004 00		
CX2769	Clocking Scheme and Data Capture"	4741	6/9/2003	4719:19; 4741:12; 6795:25; 6821:22		
CX2769	Micron Presentation, titled "Considerations for DDR-II	COOO	6/04/0000	4740.40. 4744.40. 6705.05. 6004.00		
CX2769	Clocking Scheme and Data Capture"	6822	6/24/2003	4719:19; 4741:12; 6795:25; 6821:22		
CX2773	Micron Presentation, titled "Micron Technology	2604	5/21/2003	6767:10		
CAZI13	Overview"	2004	3/21/2003	0/0/.10		
CX2775	Micron Presentation, titled "Micron Around the World"	2604	5/21/2003	JXA		
0,12770	· ·	2001	0/21/2000	0,00		
01/0770	Email from Guild to	2024	F 10.4 10.000	17/4		
CX2776	"DDR_Memory_Enablement@us.ibm.com" and	2604	5/21/2003	JXA		
	others					
CX2777	Email from Mailloux to Ryan, Lee, and Walther re: "Proposal feedback"	2604	5/21/2003	JXA		
	Floposal leedback				In Camera	
CX2780		2604	5/21/2003	JXA	4/29/2003 Order	
	Micron Datasheet: Synchronous DRAM 2 MEG x 8				4/25/2005 Oraci	
CX2792	SDRAM	2604	5/21/2003	JXA		
0)/0000	Article titled "Carpe DRAMIs Asia's dominance a	2224	E /0.4 /0.000	11/4		
CX2800	memory?"	2604	5/21/2003	JXA		
CX2828	Nvidia Folder, titled "Next Gen Memory	2050	E/20/2002	2044-17- 2046-16- 2040-24- 2050-12		
UA2828	Development"	3850	5/30/2003	3844:17; 3846:16; 3849:24; 3850:13		
	Email from Wagner to Papkipos, Moiner and others					
CX2829	re: "what was answer to the question about burst	3853	5/30/2003	3851:6; 3852:18; 3853:11		
	length on ddrs" PUBLIC					
CX2832	Nvidia Brochure	3840	5/30/2003	3838:8,14; 3839:14; 3840:10		
CX2833	Nvidia Product Overviews	3827	5/30/2003	3822:1,8; 3827:22; 3872:8		
CX2838	Document re: "SUN SDRAM SPEC"	5798	6/18/2003	5924:4,8; 5925:18; 5926:17,23; 5927:7;		
				5977:18; 5978:17		

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CX2848	Report re: "Patent Portfolio Leads to Deal of the Century"	2604	5/21/2003	JXA	iii Gamera:	Kererence
CX2851	Report re: "PC Memory Wars: DDR DRAM vs. Rambus"	2604	5/21/2003	JXA		
CX2853	Report re: "DRAM Market Forecast - Deja vu"	2604	5/21/2003	JXA		
CX2856	Infineon Presentation re: "PC133/266 APAC Technology Forum- Advanced DRAM Performance, Density and Technology"	2604	5/21/2003	JXA		
CX2864	Micron Document re: "Synchronous DRAM vs. Rambus- 1992 Annual Sales Meeting- Technology That Works for You"	2604	5/21/2003	JXA		
CX2867	Intel Document, titled "PC SDRAM Specification"	2604	5/21/2003	JXA		
CX2871	Samsung press release re: "Samsung Announces it is Developing SDRAM-II	2604	5/21/2003	JXA		
CX2883	Hyundai Presentation, titled "PC100/133 to PC266 Migration Path"	2604	5/21/2003	JXA		
CX2887	Letter from Gelsinger to Tate and Davidow	2604	5/21/2003	JXA		
CX2955	Letter from Karp to Healey attaching "Committee Letter Ballot JC-42.3-93-136 Item #557"	7785	6/27/2003	5713:22; 7784:20		
CX2957	Declaration of Joel A. Karp	2604	5/21/2003	JXA		
CX2987	Email from Armstrong to MPD Headsup re: "fwd: JEDEC (12/12/96) Trip Report"	2604	5/21/2003	JXA		
CX2995	Email from Sogas to Kostmayer, Walther, Russell and others re: "Possible meeting for AMII for 8/25 or 8/26"	2604	5/21/2003	JXA		
CX3005	Letter from Martinez to Kilbuck re: "Burst EDO/PBM" attaching Cyrix Document re: "Intel Burst Patents"	2604	5/21/2003	JXA		
CX3016	Document re: "Dual Clock Scheme for over 200 MHz Synchronous DRAM System"	2604	5/21/2003	JXA		
CX3025		2604	5/21/2003	11020:11; 11021:6; 11161:4; 11165:22; 11167:1,2	In Camera 4/29/2003 Order Designates only the following pages in camera (p. 2-3, 12, 21-22, 26-31,100, 103-107, 109, 110-114, 123, 126, 130, 137-138, 180-185)	
CX3029	Document re: "Disclosure of Issued and Pending Patents In JC-42.3"	2604	5/21/2003	10375:25; 10377:10		

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CX3131	Farmwald Infineon Trial Testimony (4/24/01)	n/a	pending	Designations submitted 8/25/03		_	
CX3132	Davidow Infineon Trial Testimony (4/24/01)	n/a	pending	Designations submitted 8/25/03			
CX3132	Vincent Infineon Deposition (1/12/01)	2189	5/19/2003	Read-in portions admitted			
CX3133	Mailloux Micron Deposition (4/5/01)	10849	7/28/2003	Read-in portions admitted			
CX3134	Mailloux Micron Deposition (4/6/01)	10849	7/28/2003	Read-in portions admitted			
CX3135	Chen FTC Deposition (1/16/03)	8737	7/14/2003	Read-in portions admitted			
CX3136	Meyer Infineon Trial Testimony (5/7/01)	2895	5/22/2003	Read-in portions admitted			
CX4001	Audio Recording of Rambus Conference Call	2604	5/21/2003	JXA			
CX4002	Video Recording of Dell Commercial	2604	5/21/2003	JXA			
CX4003	Video Recording of HP Commercial	2604	5/21/2003	JXA			
DX0001	Memory Module	Marked (not admitted) at 274	5/1/2003	274:16, 19; 291:6			
DX0002	Posters used in Respondent's opening arguments	Marked (not admitted) at 291	5/1/2003	277:13; 291:12			
DX0003	Mother Board	Marked (not admitted) at 505	5/2/2003	505:11,13; 10100:11			
DX0004	Latency Block Diagram	Marked (not admitted) at 809	5/5/2003	809:17; 869:11; 871:2,6,12; 1526:11; 1547:15,19,21,25; 1548:1; 1551:18; 1564:16,18; 1566:4; 1581:6; 1588:13; 1599:12,20			
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DX0010	Mask	Marked (not admitted) at 1163	5/7/2003	1163:15		
DX0011	Lucite	Marked (not admitted) at 1164	5/7/2003	1163:25		
DX0012	Handwritten Page: Dates of Production for Motorola	Marked (not admitted) at 1178	5/7/2003	1178:25; 1183:18		
DX0013	Slide: RDRAM vs. SDRAM	Marked (not admitted) at 1437	5/8/2003	1437:10,22; 1442:16; 1448:1		

DX0014

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Exhibit #	<u>Description</u>	Page Admitted/ Marked	Date Admitted/ Marked	Transcript Page Where Exhibit is Mentioned/JX#/Notes	In Camera?	Cross- Reference
DX0024	Rambus 1992 Org Chart	Marked (not admitted) at 2358	•			

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	PP Slide: z900 20 Processor MultiChip Module	Marked (not				
DX0052	(MCM)	admitted) at	6/12/2003	4997:5		
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D\\0050	DD OF It. Marrier Occidents and a con-	Marked (not	0/40/0000	5000.0		
DX0053	PP Slide: Memory Cards shown in z900	admitted) at 5001	6/12/2003	5002:3		
		Marked (not				
DX0054	PP Slide: Memory Cards w/ ID Labels	admitted) at	6/12/2003	5003:25; 5004:5		
DAGGGT	11 Glide. Wellioty Galas W/ 15 Eabels	5004	0/12/2000	3003.23, 3004.3		
		Marked (not				
DX0055	PP Slide: z900 Memory Card Chart	admitted) at	6/12/2003	5007:12		
	,	5007 <sup>°</sup>				
		Marked (not				
DX0056	PP Slide: z900 Main Memory Structure	admitted) at	6/12/2003	5008:2,7		
		5008				
		Marked (not		5112:22; 5132:1,6; 5275:6,9,22; 5325:5,21;		
DX0057	Handwritten list: "JEDEC Ontions" late '91-early'92	admitted) at	6/13/2003	5327:7		
	<del>•</del> • • • • • • • • • • • • • • • • • •	5112				
DVOOEO	Handwritten list: "JEDEC Options: CAS Latency" late	Marked (not	0/40/0000	5136:25; 5137:5; 5142:14,20; 5145:7;		
DX0058	'91-early'92	admitted) at 5137	6/13/2003	5276:3; 5326:2,12; 5327:7		
		Marked (not				
DX0059	Handwritten list: "JEDEC Options: Data Capture on	admitted) at	6/13/2003	5158:6,9; 5245:18; 5277:9; 5326:17,18,25		
D/(0000	Read" '95-'98	5170	0/10/2000	0100.0,0, 0240.10, 0211.0, 0020.11,10,20		
		Marked (not				
DX0060	Handwritten list: "JEDEC Options: Encreasing Speed	admitted) at	6/13/2003	5170:18; 5178:22; 5179:15; 5184:22; 5185:3;		
	of Data"	5178 <sup>°</sup>		5186:15; 5327:12,13,21		

DX0061 **SN2600667/Qtts3**8C83491;57245ad CAAS/bir4 3090n6ra2Q765rtg\Sp158468DT-D.901.26 275c Txxxx44444WT(W.4)Wattk\end(n)ot1) (T;24.240.68.68DT-D.901.25 25c Txxx04.89439WT(wa(and meitt)ed)) (at 5112

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DX0066	Alternatives to PCL #1 highlighted	admitted) at	6/16/2003	5372:23		
		5459 <sup>°</sup>				
		Marked (not				
DX0067	Fixed CAS Latency Slide	admitted) at	6/16/2003	5373:1,5		
		5459				
		Marked (not				
DX0068	Slide: Metal Mask	admitted) at	6/16/2003	5373:17; 5374:21		
		5459				
		Marked (not				
DX0069	Slide: Packaging Option	admitted) at	6/16/2003	5375:7; 5405:11		
		5459				
D\\0070	Oli la Altanati a ta DOL 110	Marked (not	0/40/0000	5070.40		
DX0070	Slide: Alternative to PCL #2	admitted) at	6/16/2003	5379:10		
		5459				
DV0074	Clida, Cat CAC Latanay	Marked (not	C/4C/2002	5270:42.45		
DX0071	Slide: Set CAS Latency	admitted) at 5459	6/16/2003	5379:12,15		
		Marked (not				
DX0072	CAS Latency w/ Fuses	admitted) at	6/16/2003	5380:9,10,12		
BAOOTE	One Editing will door	5459	0/10/2000	0000.0,10,12		
		Marked (not				
DX0073	CAS Latency Hardware w/ Fuses-laser	admitted) at	6/16/2003	5380:198,22; 5381:2		
	,	5459 <sup>°</sup>		, ,		
		Marked (not				
DX0074	CAS Latency Hardware w/ Fuses-electrical pusle	admitted) at	6/16/2003	5383:4		
		5459				
		Marked (not				
DX0075	Slide: Alternative to PCL #3	admitted) at	6/16/2003	5386:1		
		5459				
<b>5</b> \/55=5		Marked (not	-/			
DX0076	Slide: Alternative to PCL #4	admitted) at	6/16/2003	5389:13		
		5459				
DV0077	Slide: Alternative to PCL #5	Marked (not	C/4 C/2002	5393:22		
DX0077	Slide. Alternative to PCL #5	admitted) at 5459	6/16/2003	5595.22		
		Marked (not				
DX0078	Slide: Alternative to PCL #6	admitted) at	6/16/2003	5397:10		
2,0070	Side. Addition to FOL in	5459	0/10/2000	0007.10		
		Marked (not				
DX0079	Alternative to PBL	admitted) at	6/16/2003	5397:18; 5571:22; 5572:2,5		
		5459		, , , , , , , , , , , , , , , , , , , ,		

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DX0094	Alternatives to PLL/DLL	admitted) at	6/16/2003	5444:11; 5577:2,5		
		5459				
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DX0095	2nd Central Question	admitted) at	6/16/2003	5468:3		
		5581				
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DX0096	Rambus Clock Sync. Slide	admitted) at	6/16/2003	5468:5,6,8; 8644:2; 8646:23		
		5581				
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DX0097	Rambus Circuit vs PLL Diagram	admitted) at	6/16/2003	5488:10,14		
		5581				
DVOOO		Marked (not	0/40/0000	5500.00		
DX0098	3rd Central Question	admitted) at	6/16/2003	5503:23		
		5581		FF00:40: FF00:4: FF40:40: FF47:4.40:		
		Marked (not		5508:18; 5509:1; 5513:10; 5517:4,12;		
DX0099	Claim Charts - 8 pages	admitted) at	6/16/2003	5521:24; 5523:11,24; 5529:3; 5536:1,19; 5541:6; 5545:18; 5547:10; 5548:8; 5552:14;		
		5581		5557:15		
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DX0100	JX-56-114 blown up w/ Jacob's labeling	admitted) at	6/16/2003	5515:25; 5516:14; 5529:17		
DAOTOO	3X-30-114 blowif up w/ sacob's labeling	5516	0/10/2003	3313.23, 3310.14, 3323.17		
		Marked (not				
DX0101	NEC presentation on DLL enable mode with Jacob's	admitted) at	6/16/2003	5536:11		
	labeling	5536	0, 10, 200			
		Marked (not				
DX0102	Functional block diagram blow-up w/Jacob's labeling	admitted) at	6/16/2003	5555:12,19		
		5555				
		Marked (not				
DX0103	Same as DX102, but in reference to DLL	admitted) at	6/16/2003	5557:10		
		5557				
		Marked (not				
DX0104	4th Central Question	admitted) at	6/16/2003	5561:16		
		5581				
		Marked (not				
DX0105	DRAM basics graphics card	admitted) at	6/16/2003	5562:5,7; 5565:22		
		5581				
	Alternatives to PCL w/ red highlights indicating	Marked (not				
DX0106	incompatiability	admitted) at	6/16/2003	5567:23,25		
		5581				
D.V.0.4.0=	Alternatives to PBL w/ red highlights indicating	Marked (not	0/40/0005	5570.00		
DX0107	incompatiability	admitted) at	6/16/2003	5572:23		
	. ,	5581				

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DV0400	Alternatives to DEC w/ red highlights indicating	Marked (not	0/40/0000	FF70:0.40		
DX0108	incompatiability	admitted) at	6/16/2003	5576:2,12		
		5581				
DV0400	Alternatives to PLL/DLL w/ red highlights indicating	Marked (not	0/40/0000	FF77.04		
DX0109	incompatiability	admitted) at 5581	6/16/2003	5577:24		
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DX0110	Slide: Micron Products	admitted) at	6/20/2003	6262:23; 6404:25		
DAOTTO	Olide. Wildforf Froducts	6262	0/20/2003	0202.20, 0404.20		
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DX0111	Slide: Micron Technology Overview	admitted) at	6/20/2003	6267:7		
BAGTTI	Chac. Wholen rechinelegy everylew	6267	0/20/2000	0207.7		
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DX0112	Slide: Global Operations	admitted) at	6/20/2003	6270:10; 6271:15		
		6271		,		
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DX0113	Slide: International Customer Base	admitted) at	6/20/2003	6272:12		
		6272				
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DX0114	Slide: Micron's Financial Results	admitted) at	6/20/2003	11346:23,24		
		6282		11340.23,24		
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DX0115	Slide: Micron Moves Up in U.S. Patent Rankings	admitted) at	6/20/2003	6292:21; 6426:5		
		6292				
	Slide: Micron Semiconductor Technology License	Marked (not				
DX0116	Agreement	admitted) at	6/20/2003	6474:5		
	. <del>g</del>	6474				
D\(0.4.47		Marked (not	0/00/0000	6662:2,14; 6664:21; 6665:4; 6674:19;		
DX0117	Handdrawn Illustration of Data Capture Problem	admitted) at	6/23/2003	6675:15		
		6662				
DX0118	Jeff Mailloux's Intel/Rambus Response Script	Marked (not	C/04/0000	C0C0:40 44: C002:44:		
DX0118	(12/17/96)	admitted) at 6903	6/24/2003	6860:10,11; 6903:14;		
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DX0119	Handdrawn List: Four Lee Factors	admitted) at	6/24/2003	6984:21		
DAOTIS	Tiandulawii List. I oui Lee I actors	6984	0/24/2003	0904.21		
		Marked (not				
DX0120	Slide:Competitive Solutions Book Cover	admitted) at	6/25/2003	7116:22,23		
27.0120	C. ac. Copolitico Colationio Dook Co. Co.	7116	0,20,2000			
		Marked (not		6/24/2003		
DX0121	Slide: Nature of Assignment	admitted) at	6/25/2003	7125:6		
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DX0123	Slide: Key Economic Questions	admitted) at 7130	6/25/2003	7130:7; 7137:20; 7138:7		
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DX0124	Slide: Interviews conducted	admitted) at	6/25/2003	7146:25; 7149:10		
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DX0125	Slide: Materials reviewed/relied upon	admitted) at	6/25/2003	7150:9; 7151:13; 7152:24		
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DX0126	Slide: Case Study	admitted) at	6/25/2003	7163:1		
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DX0127	Slide: Summary of Conclusions #1	admitted) at	6/25/2003	7163:10		
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DX0128	Slide: Summary of Conclusions #2	admitted) at	6/25/2003	7165:20		
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DX0129	Slide: Summary of Conclusions #3	admitted) at	6/25/2003	7167:11; 7170:13; 7172:23; 7576:25		
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DX0130	Slide: Summary of Conclusions #4	admitted) at 7173	6/25/2003	7173:24		
DX0131	Slide: Summary of Conclusions #5	Marked (not admitted) at	6/25/2003	711tt		
ומוטאם	Silve. Summary of Conclusions #5	7177	0/25/2003		(DY0130) Ti 44 64 0	TD -0.0177 To 0
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DX0150	Slide: Example of Specific Investment Ex Post	admitted) at	6/25/2003	7263:3		
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DX0151	Slide: Example of Specific Investmentmine	admitted) at	6/25/2003	7263:3		
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DX0152	Slide: Example of Specific Investmentmine/money	admitted) at	6/25/2003	7264:5		
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DX0153	Slide: Example of Lock-In	admitted) at	6/25/2003	7264:10,16		
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DX0154	Slide: Example of Hold-Up	admitted) at	6/25/2003	7265:10; 7266:7; 7709:20; 7710:2		
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DX0155	Slide: Avoiding Hold-UP	admitted) at	6/25/2003	7268:16; 7269:14,17,19; 7710:4		
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DX0156	Slide: Economic Literature on Hold-Up	admitted) at	6/25/2003	7269:12		
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DX0157	Slide: Application of Hold-Up	admitted) at	6/25/2003	7270:9; 7281:13,22; 7405:14		
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DX0158	Slide: Application of Hold-up to standard setting	admitted) at	6/25/2003	7272:16; 7275:20; 7723:14,24		
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DX0159	Slide: JEDEC: IP Disclosure	admitted) at	6/25/2003	7276:3,4		
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DX0160	Slide: Application of hold-up to DRAM standard	admitted) at	6/25/2003	7281:20; 7284:16; 7406:24,25; 7408:15;		
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DX0161	Slide: Standard setting process	admitted) at	6/25/2003	7288:25		
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DX0162	Slide: Standard setting	admitted) at	6/25/2003	7290:19; 7293:3,12		
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DX0163	Slide: Standardization process (annimated)	admitted) at	6/25/2003	7294:7,14; 7296:5		
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DX0164	Slide: Standardization confers value	admitted) at	6/25/2003	7296:9; 7297:5,22		
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DX0165	Slide: Ex Ante Disclosure of IP	admitted) at	6/25/2003	7298:24		
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DX0166	setting process	admitted) at	6/25/2003	7299:21		
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DX0167	in the process	admitted) at	6/25/2003	7302:4; 10384:22; 10387:22		
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DX0168	Slide: Early disclosures leave work-around	admitted) at	6/25/2003	7303:4; 7304:7; 10388:25		
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DX0169	Slide: Ex Post Disclosure of IP	admitted) at	6/25/2003	7305:15; 10388:25		
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DX0170	Disclosure	admitted) at	6/25/2003	7305:16,24; 7307:7		
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DX0171	Slide: Nature and Scope of Market Definition	admitted) at	6/25/2003	7309:23		
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DX0172	Slide: Market Definition: Methodology	admitted) at	6/25/2003	7313:1; 7314:10		
		7313				
DV0470	Olida Madat Deficition Mathedalam (organic)	Marked (not	0/05/0000	7040:44: 7047:0		
DX0173	Slide: Market Definition: Methodology (graph)	admitted) at	6/25/2003	7316:11; 7317:9		
		7316 Marked (not				
DX0174	Clida: Taabaalagu Markata		6/25/2002	7224-16- 7225-2-24		
DA0174	Slide: Technology Markets	admitted) at 7324	6/25/2003	7324:16; 7325:2,24		
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DX0175	Slide: Technical Feasibility	admitted) at	6/25/2003	7327:12		
DAOTTO	ondo. Tooriinodi i casibility	7327	0/20/2000	1021.12		
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DX0176	Slide: Commercial Viability	admitted) at	6/25/2003	7330:19; 7331:12; 7554:9; 7566:16; 9859:11		
DAOTTO	ondo. Commoroidi Vidolity	7330	0,20,2000	. 555.75, 7551.12, 7554.5, 7555.10, 5559.11		
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DX0177	Slide: Commercial Viability Considerations	admitted) at	6/25/2003	7335:10; 7336:24; 7338:19; 7581:13;		
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DX0192 Slide: Burst Length Technology Market

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DX0206	Slide: Clock Synch Technology for On-Chip PLL/DLL	admitted) at	6/25/2003	7384:16		
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	Slide: Commercially Viable Alternatives for On-Chip	Marked (not				
DX0207	DLL	admitted) at	6/25/2003	7385:21; 7464:8		
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D\/0000	011 4 4 4	Marked (not	0/05/0000	7000 40 7000 0 00		
DX0208	Slide: Asynchronous Alternative	admitted) at	6/25/2003	7386:16; 7388:6,22		
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DX0209	Slide: Asynchronous Burst EDO	admitted) at	6/25/2003	7389:11; 9823:24		
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DX0210	Slide: Relevant Technology Markets	admitted) at	6/25/2003	7390:8,9		
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DX0211	Slide: "Cluster" Market	admitted) at	6/25/2003	7391:6		
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DX0212	Slide: Relevant Geographic Market	admitted) at	6/25/2003	7393:4		
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DV0040	Slide: Commercially Viable Alternatives for Dual-	Marked (not	0/00/0000	7400:44: 44050:40: 44054:4		
DX0213	Edge Clock	admitted) at 7402	6/26/2003	7402:14; 11353:13; 11354:4		
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DX0214	Slide: Data Acceleration Technology Market	admitted) at	6/26/2003	7403:9		
DX0214	Olide. Data Acceleration Technology Warket	7403	0/20/2003	7 +00.0		
	Slide: Data Acceleration Technology Market: Keep	00				
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DX0220	Slide: Commerically Viable Alternatives	Marked (not admitted) at 7433	6/26/2003	7433:17,19; 7434:18; 7435:3		
DX0221	Slide: Industry-Wide Coordination and Resource Commitment	Marked (not admitted) at 7436	6/26/2003	7436:4; 10141:6		
DX0222	Slide: Late Disclosure of Rambus Patents Eliminated Alternatives	Marked (not admitted) at 7437	6/26/2003	7437:18; 7438:14		
DX0223	Slide: Costs of Changing JEDEC Standards Today	Marked (not admitted) at 7440	6/26/2003	7440:4; 7443:9; 7446:24; 7447:2; 7453:2,4; 7454:18; 7456:20; 7458:8; 7716:20		
DX0224	Slide: How Long Would it Take to Create a Non-Infringing Standard? 7440:4; 7443:9; 7446	Marked (not admitted) at	6/26/2003 4·	7441:7		
DX0225	6/26/2003 7440.4, 7443.9, 7440 Slide: Difficulty of Reaching Consensus Ex Post40	, 1 471, 1 400,	т,			

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DX0234	Slide: If Rambus Had Disclosed IP to JEDEC: No RAND Letter	Marked (not admitted) at 7487	6/26/2003	7487:4; 7729:3; 7729:4		
DX0235	Slide: If Rambus Had Disclosed IP to JEDEC: RAND Letter Used	Marked (not admitted) at 7491	6/26/2003	7491:15; 7495:22; 10348:23		
DX0236	Slide: Rambus's Conduct in Actual World: Outcome of JEDEC Process	Marked (not admitted) at 7496	6/26/2003	7496:18; 7498:19		
DX0237	Slide: Rambus's Conduct in "But For" World: Outcome of JEDEC Process	Marked (not admitted) at 7499	6/26/2003	7499:1; 7500:9		
DX0238	Slide: Exclusionary Conduct: Rambus's Costly Investment	Marked (not admitted) at 7501	6/26/2003	7501:18		
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DX0240	Slide: Effects of Rambus's Conduct	Marked (not admitted) at 7632	6/26/2003	7632:15,16,25; 7638:14,68 T		
	Marked	(not 6/26/22j 6	3.12 T4Tw (7487:4	; 7729:3; 7729:/F2f BT 49.8 499.32 TD /F1 8.4D -0.025	43; 7Marked	(not ) 003

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DX0248	Slide: Undoing the Effects of Rambus's Conduct	admitted) at	6/26/2003	7518:10; 7522:7; 7752:1; 7753:11		
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DX0249	Cover of McAfee's Book	admitted) at	6/27/2003	7783:2,3		
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DX0250	Timeline: Documents and Events 1991-1996	admitted) at	6/30/2003	8031:4,7		
2710200	Timolino. Boddinone and Evente 1001 1000	8031	0,00,200	,,,		
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DX0251	Handdrawn Chart: Performance Gap	admitted) at	7/9/2003	8075:13,20		
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DX0252	Handdrawn Chart: Definition of Terms	admitted) at	7/9/2003	8082:10; 8084:2		
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DX0253	Handdrawn Chart: DRAM Chip like a Memory Card	Marked (not admitted) at	7/9/2003	8107:20		
DA0255	Handdrawn Chart. DRAW Chip like a Memory Card	8107	1/9/2003	8107.20		
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DX0254	Handdrawn Chart: Ideas that were part of Rambus'	admitted) at	7/9/2003	8121:10; 8144:5,21; 8145:6; 8146:8; 8395:2;		
	original inventions	8121		8459:24; 8460:13		
	Handdrawn List of Companies that Farmwald Met	Marked (not				
DX0255	with from 89-90	admitted) at	7/9/2003	8167:10,15; 8277:21; 8448:1,5		
		8167				
DYOSEC	Time line ( Miles Formers Idle Fordy Domehy) Activities	Marked (not	7/0/2002	04.02.42. 04.04.44		
DX0256	Timeline: Mike Farmwald's Early Rambus Activities	admitted) at 8193	7/9/2003	8193:13; 8194:11		
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DX0257	Horowitz chart-DRAM system on top and diamond	admitted) at	7/10/2003	8498:20		
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DX0258	Horowitz chart-Async. Vs. Sync.	admitted) at	7/10/2003	8503:5		
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DV00F0	Hannuita abant DDAM Variable Assass Time	Marked (not	7/40/0000	0540:40		
DX0259	Horowitz chart-DRAM Variable Access Time	admitted) at 8518	7/10/2003	8518:13		
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DX0260	Handwritten Description of Costs	admitted) at	7/14/2003	8695:17		
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DX0261	RIMM	admitted) at	7/14/2003	8695:22; 8706:10; 8721:8		
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DX0276	Slide: Alternatives to Programmable CAS Latency - 6. Stay with asynchronous DRAM (e.g., burst EDO)	Marked (not admitted) at 9365	7/18/2003	9365:20		
DX0277	Slide: Alternatives to Programmable Burst Length - 1. Use fixed burst length parts	Marked (not admitted) at 9368	7/18/2003	9368:15		
DX0278	Slide: Alternatives to Programmable Burst Length - 2. Program burst length by blowing fuses on the DRAM	Marked (not admitted) at 9370	7/18/2003	9370:11		
DX0279	Slide: Alternatives to Programmable Burst Length - 3. Use an existing pin or a new, dedicated pin to identify burst length	Marked (not admitted) at 9371	7/18/2003	9371:3		
DX0280	Slide: Alternatives to Programmable Burst Length - 4. Explicitly identify burst length in the read command	Marked (not admitted) at 9374	7/18/2003	9374:9		
DX0281	Slide: Alternatives to Programmable Burst Length - 5. Use a burst terminate command	Marked (not admitted) at 9374	7/18/2003	9374:9		
DX0282	Slide: Alternatives to Programmable Burst Length - 6. Use a CAS pulse to control data output	Marked (not admitted) at 9377	7/18/2003	9377:22		
DX0283	Slide: Alternatives to Dual-Edge Clocking - 1. Use two or more interleaved memory banks on-chip	Marked (not admitted) at 9381	7/18/2003	9381:23; 9386:2		
DX0284	Slide: Alternatives to Dual-Edge Clocking - 2. Keep each DRAM single data rate and interleave banks on the module (DIMM)	Marked (not admitted) at 9387	7/18/2003	9387:22; 9388:2		
DX0285	Slide: Alternatives to Dual-Edge Clocking - 3. Increase the number of pins per DRAM 4. Increase the number of pins per module	Marked (not admitted) at 9391	7/18/2003	9391:15		
DX0286	Slide: Alternatives to Dual-Edge Clocking - 5. Double the Clock Frequency	Marked (not admitted) at 9393	7/18/2003	9393:5,17		
DX0287	Slide: Alternatives to Dual-Edge Clocking - 6. Use simultaneous bidirectional I/O drivers	Marked (not admitted) at 9395	7/18/2003	9395:22		
DX0288	Slide: Alternatives to Dual-Edge Clocking - 7. Use toggle mode	Marked (not admitted) at 9398	7/18/2003	9398:12		
DX0289	Slide: Alternatives to On-Chip PLL/DLL - 1. Put the DLL on the Memory Controller	Marked (not admitted) at 9405	7/18/2003	9405:12,13		

Exhibit #	Description	Page Admitted/ Marked	<u>Date Admitted/</u> Marked	Transcript Page Where Exhibit is  Mentioned/JX#/Notes	In Camera?	Cross-
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DX0304	Slide: How are Standards Set?	Marked (not admitted) at 9788	7/22/2003	9788:16		
DX0305	Slide: Intel Created the De Facto Standard for SDRAM	Marked (not admitted) at 9796	7/22/2003	9796:22; 10026:13		
DX0306	Slide: Proposed Alternatives to Rambus's Technology in SDRAM	Marked (not admitted) at 9810	7/22/2003	9810:13		
DX0307	Slide: Proposed Alternatives to Rambus's Technology in SDRAM: Cost Penalties	Marked (not admitted) at 9811	7/22/2003	9811:20; 10216:9; 10221:4,12; 11253:2,6,13		
DX0308	Slide: Proposed Alternatives to Rambus's Technology in SDRAM: Disadvantages	Marked (not admitted) at 9819	7/22/2003	9819:16,17; 10226:16		
DX0309	Slide: Proposed Alternatives to Rambus's Technology in SDRAM: Cost Penalties	Marked (not admitted) at 9824	7/22/2003	9824:1; 10216:23; 10221:2; 11254:4,14		
DX0310	Slide: Proposed Alternatives to Rambus's Technology in SDRAM: Disadvantages	Marked (not admitted) at 9828	7/22/2003	9828:2; 9829:16; 10220:3,6; 10226:23		
DX0311	Slide: Proposed Alternt8 f.59t T.8 0 2wisa9dy.72		61anthw (Sts0.08 -1	0Associ5.96 with.4 TD -0.0132 T828:52 N057C	Cov24 d2 15.9T*6	Гс 0.044 Tw (Mark

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DX0318	Slide: Rambus has Acquired no Additional Market Power	Marked (not admitted) at 9899	7/22/2003	9899:20		
DX0319	Slide: There is No Harm to Competition Because Disclosure Would Not Have Affected the Outcome	Marked (not admitted) at 9907	7/22/2003	9907:8,9		
DX0320	Slide: Economics of Predation and Exclusion	Marked (not admitted) at 9909	7/22/2003	9909:18; 10163:11,17		
DX0321	Slide: Why it Made Economic Sense for Rambus to Avoid Disclosure of Unprotected IP	Marked (not admitted) at 9914	7/22/2003	9914:23; 9916:11; 9917:13; 10164:4,6; 10166:23,25; 10170:5; 10171:8; 10174:14; 10179:12,14; 10181:5; 10189:6,22; 10190:9		
DX0322	Slide: Summary of Conclusions	Marked (not admitted) at 9940	7/22/2003	9940:8,19		
DX0323	Handwritten list: Conditions necessary for opportunism	Marked (not admitted) at 9985	7/22/2003	9984:23,25; 10009:14; 10010:19; 10011:15; 10012:25; 10013:11,17; 10016:13,23; 10017:5		
DX0324	Handwritten list: Materials not reviewed by Dr. Rapp	Marked (not admitted) at 10002	7/22/2003	10002:22,24		
DX0325	Slide: Reliability	Marked (not admitted) at 10004	7/22/2003	10004:6; 10006:13; 10261:20		
DX0326	Handwritten list: Revolutionary Technology	Marked (not admitted) at 10081	7/23/2003	10081:12,14		
DX0327	Handrawn "Matrix"	Marked (not admitted) at 10081	7/23/2003	10084:25		
DX0328	Handwritten list: Share of leading DRAM technology	Marked (not admitted) at 10100	7/23/2003	10100:12,13,14		
DX0329	Slide: Scope of Tasks	Marked (not admitted) at 10306	7/24/2003	10306:19		
DX0330	Slide: Subsidiary Tasks	Marked (not admitted) at 10308	7/24/2003	10308:20		
DX0331	Slide: Summary of Key Conclusions	Marked (not admitted) at 10311	7/24/2003	10275:5,13; 10311:17; 10485:5; 10487:5; 10548:8		

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Exhibit #	<u>Description</u>	Marked Marked (not	<u>Marked</u>	Mentioned/JX#/Notes	In Camera?	Reference
DX0368	Slide: Effect of Considering Productivity Gains	Marked (not admitted) at	8/1/2003	11267:17; 11272:9		
DX0300	Silde. Effect of Considering Froductivity Cams	11267	0/1/2003	11207.17, 11272.3		
	Slide: Assuming 30% Productivity Gains Overturns	Marked (not				
DX0369	Dr. Rapp's Results for SDRAM (percentage royalty	admitted) at	8/1/2003	11272:23; 11377:22		
	rates)	11272				
	Slide: Assuming 30% Productivity Gains Overturns	Marked (not				
DX0370	Dr. Rapp's Results for SDRAM (percentage royalty	admitted) at	8/1/2003	11275:14		
	rates)	11275				
DV0074	Slide: Dr. Rapp's Lock-in / Switching Cost Analysis Is	Marked (not	0/4/0000	44000:0 44: 44005:04 05		
DX0371	Inaccurate	admitted) at 11282	8/1/2003	11282:8,11; 11295:21,25		
		Marked (not				
DX0372	Slide: Dr. Rapp's Analysis Substantially Understates	admitted) at	8/1/2003	11285:23; 11287:23; 11289:23; 11293:7		
271001.2	the Relevant Switching Costs	11285	o, .,			
	Clide: Dr. Dennie Angliscia Israeros Coordination	Marked (not				
DX0373	Slide: Dr. Rapp's Analysis Ignores Coordination Costs / Difficulties	admitted) at	8/1/2003	11296:14,18; 11297:19		
	Costs / Difficulties	11296				
		Marked (not				
DX0374	Slide: Relevant Coordination Costs / Difficulties	admitted) at	8/1/2003	11302:6; 11303:15		
		11302				
DX0375	Slide: Professor Teece's "But-For" World Analysis is	Marked (not admitted) at	8/1/2003	11305:16,17		
DX0375	Flawed	11305	0/1/2003	11303.10,17		
		Marked (not				
DX0376	Slide: Summary of Rebuttal Conclusions	admitted) at	8/1/2003	11316:9		
		11316				
		Marked (not				
DX0377	Altered Version of "But-For World Decision Tree"	admitted) at	8/1/2003	11306:23; 11307:20,21; 11318:11		
		11306				
DV0070						
DX0378						

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DX0382	CD of demonstratives used with Rhoden	Marked (not admitted) at 11383	8/1/2003	11383:11,12		
JX0001	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 54 (Cambridge, MA)	2604	5/21/2003	JXC		
JX0002	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 55 (Denver, CO)	2604	5/21/2003	JXC		
JX0003	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 56 (Las Vegas, NV)	2604	5/21/2003	JXC		
JX0004	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 57 (San Diego, CA)	2604	5/21/2003	JXC		
JX0005	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 58 (Anchorage, AK)	2604	5/21/2003	2752:23; 2758:15		
JX0006	JEDEC Council Minutes of Meeting No. 87 (Pittsburgh, PA)	2604	5/21/2003	2402:15; 2754:15; 2759:5		
JX0007	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 59 (Philadelphia, PA)	1358	5/8/2003	1356:19; 1358:10		
JX0007	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 59 (Philadelphia, PA)	2604	5/21/2003	JXC		
JX0008	JEDEC Council Minutes of Meeting No. 88 (San Francisco, CA)	2604	5/21/2003	JXC		
JX0009	JC-42.5 Committee on Memory Cards and Modules, Minutes of Meeting No. 8 (Maui, HI)	2604	5/21/2003	JXC		
JX0010	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 60 (Maui, HI)	450	5/1/2003	416:2,7; 425:2; 450:2; 778:8,11,25; 779:9; 896:21,24; 1262:24; 1263:2; 1372:18; 2437:20; 2504:21; 3015:17; 3023:18,18; 3024:2,16; 3033:14,18; 3035:3,4,17, 19; 3036:6, 25; 3037:5; 5099:8, 16; 5100:1; 5101:19; 5105:21; 5106:2, 4, 8; 5172:15; 7926:19; 7928:4		
JX0010	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 60 (Maui, HI)	2604	5/21/2003	JXC		
JX0011	JEDEC Council Minutes of Meeting No. 89 (Phoenix, AZ)	2604	5/21/2003	2409:23; 2411:16		
JX0012	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 61 (Seattle, WA)	1384	5/8/2003	1382:5; 1384:2; 2507:3, 11; 2510:2; 3039:10; 3040:20; 7930:10, 23		
JX0012	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 61 (Seattle, WA)	2604	5/21/2003	JXC		

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JX0021	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 72 (Albuquerque, NM)	469	5/1/2003	464:13, 17, 19; 465:2; 469:4; 1405:13; 1586:16, 17; 1592:16, 21; 2568:13; 3199:12; 3514:11, 13; 5533:8, 11, 23; 5634:21; 7964:20, 23		
JX0021	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 72 (Albuquerque, NM)	2604	5/21/2003	JXC		
JX0022	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 73 (Maui, HI)	2604	5/21/2003	10939:21, 24; 10940:3, 15; 10942:1; 10948:1		
JX0023	JC-42.3 DRAM Task Group Meeting (San Jose, CA)	839	5/5/2003	824:18; 838:24		
JX0023	JC-42.3 DRAM Task Group Meeting (San Jose, CA)	2604	5/21/2003	JXC		
JX0024	JC-42 Executive Committee Meeting (Las Vegas, NV)	2604	5/21/2003	JXC		
JX0025	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 74 (Las Vegas, NV)	2498	5/21/2003	930:16; 2484:20; 2498:17; 3243:7		
JX0025	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 74 (Las Vegas, NV)	2604	5/21/2003	JXC		
JX0026	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 75 (New Orleans, LA)	475	5/1/2003	468: 22, 24; 469:12, 20; 475:4; 1014:4, 7, 8, 13; 1014:4; 1015:9; 1024:11; 1039:23; 1042:9; 1407:11, 18; 1704:22; 1705:4; 2571:21; 2572:12, 14; 2574:8; 5088:22, 25; 5089:8, 12; 7972:19		
JX0026	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 75 (New Orleans, LA)	2604	5/21/2003	JXC		
JX0027	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 76 (Crystal City, VA)	1426	5/8/2003	1410:14; 1423:20, 23; 1426:9; 1456:11; 2577:1, 10; 2578:2, 18; 3305:23; 3306:18; 3323:12; 3550:3; 5092:13, 24; 5093:12; 5094:12; 5095:21; 5096:2, 12; 7924:6, 10; 7981:14, 17, 22		
JX0027	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 76 (Crystal City, VA)	2604	5/21/2003	JXC		
JX0028	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 77 (Dallas, TX)	484	5/1/2003	350:16, 20, 21, 24; 351:1, 3; 353:11, 20; 474:21, 25; 475:1, 1, 11; 484:1; 570:8; 956:10; 957:3, 6, 8, 22; 958:2; 968:19; 1028:22; 1419:23; 1420:17, 19; 1422:23; 1714:21; 2722:3, 7; 3339:10; 5543:12; 7985:7; 10975:21		
JX0028	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 77 (Dallas, TX)	2604	5/21/2003	JXC		
JX0029	Minutes of Interim Meeting JC-42.3 Committee (Sunnyvale, CA)	489	5/1/2003	484:9, 13; 485:6; 4895; 3363:21, 24; 3366:14; 6627:13, 15, 22; 6655:11, 17; 6663:15; 6988:14, 16, 17		

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JX0029	Minutes of Interim Meeting JC-42.3 Committee (Sunnyvale, CA)	2604	5/21/2003	JXC		
JX0030	Minutes of the JC-42 Executive Committee (San Diego, CA)	2604	5/21/2003	JXC		
JX0031	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 78 (San Diego, CA)	515	5/2/2003			

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JX0045	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 89 (San Diego, CA)	2604	5/21/2003	JXC		
JX0046	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 90 (Las Vegas, NV)	2604	5/21/2003	JXC		
JX0047	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 91 (Vancouver, BC)	2604	5/21/2003	JXC		
JX0048	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 92 (Toronto, Canada)	2604	5/21/2003	JXC		
JX0049	JC-42.3 Committee on DRAM Memories, Minutes of Special Meeting (Milpitas, CA)	2604	5/21/2003	JXC		
JX0050	JEDEC Board of Directors (Council) Minutes of Meeting No. 116 (Orlando, FL) (uncorrected version)	2604	5/21/2003	JXC		
JX0051	JEDEC Board of Directors (Council) Minutes of Meeting No. 117 (Arlington, VA)	2604	5/21/2003	JXC		
JX0052	JC-42.3 Committee on DRAM Memories, Minutes of Meeting No. 97 (Kona, HI)	2604	5/21/2003	6799:23; 6800:3, 8		
JX0053	Manual for Committee, Subcommittee, and Working Group Chairmen and Secretaries, Engineering Publication: EP-3-F	2604	5/21/2003	JXC		
JX0054	Style Manual for Standards and Publications of EIA, TIA and JEDEC: EP-7-A	353	5/1/2003	345:21; 346:23; 347:16; 353:4; 642:20; 1877:13; 1879:13; 1884:6; 1887:3, 5; 1905:21; 2079:7; 2088:3		
JX0054	Style Manual for Standards and Publications of EIA, TIA and JEDEC: EP-7-A	1884	5/14/2003	345:21; 346:23; 347:16; 353:4; 642:20; 1877:13; 1879:13; 1884:6; 1887:3, 5; 1905:21; 2079:7; 2088:3		
JX0054	Style Manual for Standards and Publications of EIA, TIA and JEDEC: EP-7-A EIA Engineering Publication: Style Manual for	2604	5/21/2003	JXC		
JX0055	, , , , , ,					

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Exhibit #	<u>Description</u>	<u>Marked</u>	<u>Marked</u>	Mentioned/JX#/Notes	In Camera?	Reference
JX0057	JEDEC Standard, Double Data Rate (DDR) SDRAM Specification, JESD79	1306	5/7/2003	1292:25; 1293:13; 1294:4, 5, 14, 18; 1297:19; 1306:19; 1426:23; 1428:13, 14; 4391:14, 21; 4392:1, 8; 4394:2, 18, 25; 4395:9; 4396:15, 24; 4397:11, 16; 4497:20; 4498:2; 5171:3; 5172:3, 8, 9; 5175:23; 5176:1, 15; 5190:14; 5551:16, 19, 25; 5554:4		
JX0057	JEDEC Standard, Double Data Rate (DDR) SDRAM Specification, JESD79	2604	5/21/2003	JXC		
JX0058	JEDEC Standard, Configurations for Solid State Memories, JEDEC Standard No. 21-C, Release 10	2604	5/21/2003	JXC		
JX0059	JEDEC Committee Ballot: JC-42.3-92-85	357	5/1/2003	354:4, 8, 11; 355:4; 357:15; 446:10; 447:17; 451:13; 581:8; 1389:25		
JX0059	JEDEC Committee Ballot: JC-42.3-92-85	2604	5/21/2003	JXC		
JXA	List of admitted exhibits	2604	5/21/2003	2603:20, 23		
JXB	List of admitted exhibits	2604	5/21/2003	2603:21, 24		
JXC	List of admitted exhibits	2604	5/21/2003	2603:21, 25		
RX0001	Japanese document regarding meeting 3/5 to 3/6 Santa Clara University (Translation to be supplied)	2604	5/21/2003	JXB		
RX0001A	Translated version of RX0001	2604	5/21/2003	JXB		
RX0008	Micron DRAM Update	2604	5/21/2003	JXB		
RX0009	Manual for Committee, Subcommittee, and Working Group Chairmen and Secretaries, Engineering Publication EP-3-F October 1981	4073	6/3/2003	2215:22; 4073:18		
RX0012	Document titled JEDEC Module Standards Requirements: Proposal	2604	5/21/2003	JXB		
RX0015	RamBus Business Plan, 6/26/89	2604	5/21/2003	145:22; 8100:9,12; 8102:13; 8107:25; 8108:15; 8111:16; 8121:15; 8126:17; 8129:12; 8450:23		CX0570
RX0019	RamBus Business Overview	2604	5/21/2003	JXB		CX1282
RX0021	RAMBUS TECHNOLOGY OVERVIEW	2604	5/21/2003	JXB		CX1284
RX0022	RamBus Technology Overview	2604	5/21/2003	JXB		CX1284
RX0024	NON - DISCLOSURE AGREEMENT	8151	7/9/2003	8151:2,12		
RX0025	Fax with attached Rambus Technology Overview	2604	5/21/2003	146:16; 160:5; 8161:5,6,8,10; 8162:12,23		
RX0029	Rambus Technology Overview by Horowitz	2604	5/21/2003	JXB		
RX0031	Horninger handwritten notes re Rambus	2604	5/21/2003	JXB		
RX0036	Horninger fax to Michael re Rambus technology	2604	5/21/2003	JXB		
RX0037	Horninger fax to Michael re Rambus technology	2604	5/21/2003	JXB		
RX0042	Memo re visit of Davidow on 3/19/90	2604	5/21/2003	JXB		
RX0043	Notes re conversation with Rambus	2604	5/21/2003	JXB		
RX0044	Notes re conversation with Rambus	2604	5/21/2003	JXB		
RX0048	Penzel memo re Rambus	2604	5/21/2003	JXB		

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	Electronic Design, Memory-CPU Interface speeds up					
	data transfers; data transfers of 500 MBYTES/S are					
D\/0000	possible as a novel DRAM architecture and bus run	0004	E (0.4 (0.000	17/2		
RX0238	at 250 MHZ with 600-MV logic levels. (Rambus	2604	5/21/2003	JXB		
	Inc.'s Rambus Channel Bus) (Product Innovation)					
	(Central Processing Unit; Dynamic Random Access					
	Memory)		= /2.2 /2.2.2			
RX0240	Memo to Kalter re Rambus	2766	5/22/2003	2627:17; 2766:17		
RX0250	Email to Kellogg re Synchronous DRAM	5350	6/16/2003	5313:9,13; 5350:20		
RX0258	Email from G. Cloud to M. Alder and others re	2604	5/21/2003	JXB		
	Rambus					
RX0261	IBM internal memo re Rambus meeting	2604	5/21/2003	5311:16		
RX0262	[Involvement in Rambus for Over Two Years]	2604	5/21/2003	JXB		
RX0263	IBM memo re Rambus	2604	5/21/2003	JXB	CX2368	
RX0269	Meyer report of IBM/Siemens Conference re SDRAM	2604	5/21/2003	JXB		
RX0272	Memo re Rambus/IBM meeting	2604	5/21/2003	JXB		
RX0273	Letter regarding Meeting on 04/23/1992	2766	5/22/2003	2631:19; 2766:17		
RX0279	CMOS V Luna Development, Rambus Assessment, Unique Rambus Features/Attributes	2604	5/21/2003	JXB		
DVOOG	Meyer/Wirth report: SYNCHRONOUS DRAM	0004	E /04 /0000	IVD		
RX0285	Report on Specification, Feasibility, Cost.	2604	5/21/2003	JXB		
RX0285A	Translated version of RX0285	2604	5/21/2003	212:3; 4510:23; 4511:3		
RX0286	Meyer memo re Siemens/IBM telephone conference	2604	5/21/2003	JXB		
RX0286A	Meyer memo re Siemens/IBM telephone conference (TRANSLATION)	2604	5/21/2003	2641:22; 2642:5; 4516:11; 5662:9; 5663:13		
RX0289	Meyer chart outlining the Pros and Cons of Sync	2604	E/24/2002	JXB		
KAU209	DRAM, Rambus DRAM, and Cached DRAM	2604	5/21/2003	JAB		
RX0289	Meyer chart outlining the Pros and Cons of Sync	2766	5/22/2003	214:1; 2529:20; 2766:16; 4518:13		
KAU209	DRAM, Rambus DRAM, and Cached DRAM	2700	5/22/2003	214.1, 2529.20, 2766.16, 4516.15		
RX0290	Handwritten JEDEC JC-42.3 Meeting Minutes, New	2604	5/21/2003	2660:16; 5087:12; 5319:20; 5322:11;		CX2370
10290	Orleans, 5/7/92	2004	3/21/2003	5663:21; 5665:14		CA2370
RX0293	Email regarding non-disclosure agreements	3493	5/29/2003	3493:11,19		
	Memo to Joel Karp and others from Gil Russell re JC-	•				
RX0294	42.1/.2/.3/.5 & JC-16 Low Voltage Interface JEDEC	2604	5/21/2003	JXB		
	Committee Meeting, New Orleans					
	Meyer trip report and meeting summary from					
RX0297	JEDEC's New Orleans meetings on May and 5, 1992	2604	5/21/2003	2663:23		
D\/065=1	Meyer trip report and meeting summary from	000.	E/04/2222	WD.		
RX0297A	JEDEC's New Orleans meetings on May and 5, 1992	2604	5/21/2003	JXB		
	(TRANSLATION AND CERTIFICATION)					

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RX0301	1992 Symposium on VLSI Circuits	2604	5/21/2003	8552:1,2; 8554:20; 8623:1,1,6,10; 8625:16		
	IBM/SIEMENS chart: "Compare Alternatives for					
RX0303	Future High Performance, High Volume DRAM	2604	5/21/2003	JXB		
	Designs"					
	IBM/SIEMENS chart: "Compare Alternatives for					
RX0303	Future High Performance, High Volume DRAM	2766	5/22/2003	2544:14; 2547:1; 2659:17; 2766:16; 5323:21		
	Designs"					
RX0307	Handwritten Meeting Notes re Gil Russell: ST Market	7813	6/27/2003	7813:2.13		
KA0307	Seminar	7013	0/21/2003	7013.2,13		
	Memo to Joel Karp and others from Gil Russell re					
RX0315	JEDEC JD-16/42.3/42.5 Meeting, Denver, Colorado	2604	5/21/2003	JXB		
	JEDEC 3D-10/42.3/42.3 Meeting, Deriver, Colorado					
	SVPC "Synchronous DRAMS Break The Memory					
RX0316	System Bandwidth Bottleneck," by Powell and Vogley	2604	5/21/2003	JXB		
	. , , , ,					
RX0317	Micron presenation slides entitled SDRAMS v.	979	5/5/2003	848:19; 849:1; 850:5; 979:2		
	Rambus for 1992 Annual Sales Meeting					
RX0320	Business Plan (draft) for 92-97	2604	5/21/2003	147:14		
RX0321	Meyer presentation to Schumacher and others titled	2604	5/21/2003	2881:1; 4520:23; 4522:14; 4524:17		
	What is Rambus?			, , ,		
RX0324	JEDEC JC-42.X and JC-16 Low Voltage Meeting Minutes DC 09/14/1992 to 09/18/1992	2604	5/21/2003	JXB		
	PLL DESIGN FOR A 500 MBYTES / SEC					
RX0327	INTERFACE	2604	5/21/2003	JXB		
	IEEE Spectrum October 1992, Fast computer					
RX0329	memories	8980	7/16/2003	8973:16; 8976:21; 8978:7; 8980:6		
	IEEE Spectrum, October 1992, Fast DRAMS for					
RX0330	sharper TV	8981	7/16/2003	8980:14; 8981:9		
	IEEE Spectrum October 1992, A new era of faxt					
RX0331	dynamic RAMs	2604	5/21/2003	JXB		
D\/0000	IEEE Spectrum October 1992, A fast path to one	0000	7/40/0000	0000 00 0000 4 44 0000 44		
RX0332	memory	8989	7/16/2003	8986:20; 8988:1,14; 8989:14		
RX0333	IEEE Spectrum October 1992, Fast interfaces for	0000	7/40/2002	0004.47, 0000.42		
KX0333	DRAMs	8986	7/16/2003	8981:17; 8986:13		
RX0335	Vincent letter to Wombell re EPO practice	7901	6/30/2003	7893:18; 7895:13; 7901:12		
RX0340	Micron presentation entitled SDRAMs v. Rambus	978	5/5/2003	840:15,20; 978:19		
RX0340	Micron presentation entitled SDRAMs v. Rambus	2604	5/21/2003	JXB		
RX0347	NEC Rambus update slide presentation	2604	5/21/2003	JXB		
	Electronic Engineering Times, Design, Designers Try					
RX0350	Alternatives to Asynchronous DRAMS: New	2604	5/21/2003	JXB		
	approach to memory needed					
RX0356	Meeting Minutes Regarding JC-42.5 JEDEC Cards	978	5/5/2003	788:4; 884:10; 886:16,20; 888:12,20; 890:22;		
	And Modules Fort Lauderdale, FL 12/08/1992	- · •	-: -: <b></b>	978:9		

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Fyhihit #	Description	Page Admitted/	Date Admitted/	<u>Transcript Page Where Exhibit is</u> Mentioned/JX#/Notes	In Comercia	Cross-
Exhibit # RX0361	<u>Description</u> Fusco JEDEC meeting report to Williams	<u>Marked</u> 978	<u>Marked</u> 5/5/2003	<u>Mentioned/JX#/Notes</u> 885:5,9; 886:20,25; 978:4	In Camera?	Reference
KAU361	Memorandum from Kolankowsky to Nohilly re	978	5/5/2003	885:5,9; 886:20,25; 978:4		
RX0373	Rambus	2604	5/21/2003	JXB		
	Micron document titled Synchronous DRAMS vs.					
RX0374	Rambus, 3/93	2604	5/21/2003	JXB		
DV0077	Memo re: JEDEC DRAM/SDRAM Meeting Minutes -	0004	E /04 /0000	IVD		
RX0377	March 1993	2604	5/21/2003	JXB		
RX0379	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX0379A	Translated version of RX0379	2604	5/21/2003	JXB		
	Article Regarding DRAMs For New Memory Systems					
RX0382	(Part 2) Rambus, Ramlink Offer Revolutionary New	2604	5/21/2003	JXB		
	Interfaces					
RX0384	Meyer memo to Woldan (Siemens)	2604	5/21/2003	2882:16		
	IEEE Journal of Solid-State Circuits article on "A 500					
RX0385	Megabyte(s) Data-Rate 4.5 DRAM" by Rambus and	2604	5/21/2003	8556:7; 8636:13		
DV0000	Toshiba	0004	F /04 /0000	7044-0-4		
RX0388 RX0390	Handwritten Meeting Notes titled Rambus Fax from Ludlow to Townsend regarding patents	2604 2604	5/21/2003 5/21/2003	7814:3,4 JXB		
RX0390	Fax re patents traced by JEDEC	2604	5/21/2003	JXB		
RX0391	Committe Survey Ballot - Mode Register Read	2604	5/21/2003	587:3		
11/10392	Minutes of the Meeting of the ANSI Ad Hoc Patent	2004	3/21/2003	307.3		
RX0398	Group Friday 05/21/1993 100 Maiden Lane New York	2604	5/21/2003	JXB		
100000	New York	2004	3/2 1/2003	ONE .		
RX0403	Hitachi Visit Notes	2604	5/21/2003	JXB		
RX0404	licensing update	2604	5/21/2003	JXB		
	Japanese document with English translation titled					
RX-0406	Rambus Patent (summary of responses) 6/10/93	6821	6/24/2003	5667:18; 6820:14		
	written by Sakao (Translation to be supplied)					
RX0411	Handwritten meeting notes titled Telecon re	7814	6/27/2003	7914:12:10		
KA0411	telephone conversation with Rambus	7014	0/21/2003	7814:13,19		
RX0416	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX0416A	Translated version of RX0416	2604	5/21/2003	JXB		
RX0419	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX0419A	Translated version of RX0419	2604	5/21/2003	JXB		
RX0420	Fax re BGA Patent/License Rights	2604	5/21/2003	182:2; 2458:23; 2728:5		
RX0421	Memo from Gordon Kelley to Jim Townsend and	594	5/2/2003	592:10; 593:20; 594:1,3,5,18		part of
- · · · ·	others re BGA Patent/License Rights			., , . , . , . , . , . , . , . ,		RX0420
RX0421	Memo from Gordon Kelley to Jim Townsend and	2604	5/21/2003	JXB		part of
	others re BGA Patent/License Rights					RX0420
RX0422	Email re August 1993 meeting in Asia of SRAM	2604	5/21/2003	JXB		
	suppliers					

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	SEMICONDUCTOR TECHNIOLOGY LICENSE					

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SEMICONDUCTOR TECHNOLOGY LICENSE

RX0538 AGREEMENT BETWEEN NEC CORPORATION

AND RAMBUS INC.

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RX0605	Fax letter from Donohoe re Samsung-Micron Agreement and letter to Appleton from Yoon Woo Lee	2604	5/21/2003	JXB		
RX0608	SyncLink Consortium notes 9/19/95	2604	5/21/2003	JXB		
RX0610	Email from Gustavson entitled "Consortium Minutes"	2604	5/21/2003	JXB		
RX0614	Memo from McGrath re JEDEC JC-42 and JC-16	2604	5/21/2003	JXB		
RX0615	Letter re JEDEC JC42.3 DRAM Meeting Report, Sept. 11-14, 1995	2604	5/21/2003	JXB		
RX0616	Style Manual for Standards & Publications of EIA and JEDEC, EP-7-B	2083	5/15/2003	2081:10; 2082:13; 2083:18; 2146:14		JX0055
RX0619	Report Regarding 64-Megabit Rambus DRAM Product Summary	2604	5/21/2003	JXB		
RX0620	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX0620A	Translated version of RX0620	2604	5/21/2003	JXB		
RX0622	Mailloux email to Cloud, Bedard, Kehne, Lowrey and Walther	2604	5/21/2003	JXB		
RX0625	Agreement Containing Consent Order to Cease and Desist In the Matter of Dell Computer Corp	2604	5/21/2003	JXB		
RX0629	Memo from Malloux re RAMBUS Inc. patents	977	5/5/2003	231:18; 944:23; 977:17; 6524:22; 6525:2,3,11; 6606:15,18; 6607:7; 6610:6; 6828:25		
RX0630	Memo re Rambus Inc. patents (w/ abstracts)	2604	5/21/2003	10905:3		
RX0643	Rambus Pre-Meeting Briefing Business and Marketing Issues	2604	5/21/2003	JXB		
RX0646	Single Page Handwritten Document titled Intel, PC-SDRAM, 12/19/95	2604	5/21/2003	JXB		
RX0653	Declaration of Gerald T. Lane	2604	5/21/2003	2608:23		
RX0655	Gustavson e-mail with minutes of 12/12/95 Synclink meeting.	2604	5/21/2003	JXB		
RX0659	Letter Regarding Revised Agreement Reflecting Points Raised	2604	5/21/2003	JXB		
RX0660	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX0660A	Translated version of RX0660	2604	5/21/2003	JXB		
RX0663	SyncLink Consortium Confidential Minutes of 1/11/96 meeting	978	5/5/2003	232:11; 947:4; 977:24; 2736:11; 6525:18,23; 6528:8; 6532:2; 6544:10		
RX0664	Request for Intel's view of memory standardization.	2604	5/21/2003	JXB		
RX0668	IEEE Comments to FTC RE Dell consent decree	2604	5/21/2003	JXB		
RX0669	Letter from Bart and Kelly to FTC re In the Matter of Dell Computer Corporation	652	5/2/2003	605:11; 2013:13; 2014:16; 2016:18; 2019:7; 2021:15; 2022:4; 2093:20; 2106:5; 2118:14; 2121:4		

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RX0674	JEDEC Special Meeting of JC-42.3B DRAM SDRAM on 01/31/0000	2766	5/22/2003	2732:3; 2766:16		
RX0675	Email from Gustavson re IEEE Patent Policy	9297	7/17/2003	9234:16; 9294:19; 9297:22,25		
RX0676	Email from Gustavson re Patent Policy	9294	7/17/2003	9234:17; 9292:20; 9294:4,7		
RX0691	JC 42 Plenary meeting minutes	2144	5/15/2003	911:17; 2083:25; 2144:8		
RX-0694	SyncLink Consortium meetings minutes 3/29/96 at Hyundai	9110	7/16/2003	9034:16; 9110:16		
RX-0695		4982	6/12/2003	3563:21,23; 3583:13; 4981:25	In Camera 4/29/2003 Order	
RX0697	IBM trip report re 03/26/1996 JEDEC meeting	2604	5/21/2003	JXB		
RX0700	Letter regarding JEDEC Invoice	2604	5/21/2003	JXB		
RX0701	Chart regarding Rambus US And Foreign Patents	2604	5/21/2003	JXB		
RX0710	SyncLink Consortium Meeting Minutes at Santa Clara University	2604	5/21/2003	JXB		
RX0720	SyncLink Consortium Meeting minutes at Santa Clara University	2604	5/21/2003	JXB		
RX0731	Email string re SyncLink	2604	5/21/2003	JXB		
RX0735	Fax re Rambus withdrawal from JEDEC	2604	5/21/2003	JXB		
RX0736	SyncLink Consortium Meeting, Santa Clara	2604	5/21/2003	JXB		
RX0740	Letter re: Dell Computer	652	5/2/2003	617:14		
RX0742	FTC Consent Order on Dell	1087	5/7/2003	606:23; 608:12; 614:14; 616:25; 1076:2,3; 1087:10; 2111:11,13		
RX0742 RX0744	FTC Consent Order on Dell	2604	5/21/2003	JXB		

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RX0779	Vogley Notes re SyncLink Consortium meeting 10/1/95	2604	5/21/2003	JXB		
RX0780	SyncLink minutes	2604	5/21/2003	6877:21		
RX0781		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX0782	Micron document re: Rambus meeting	2604	5/21/2003	JXB	1/20/2000 0. 00.	
RX0785	Mosaid e-mail to Tabrizi re royalties due to Mosaid	2604	5/21/2003	9129:11		
RX0786 RX0787	MEETING ON SYNCHRONOUS DRAM Memo re BGA packages	2604 2604	5/21/2003 5/21/2003	JXB JXB		
RX0788	Russell fax regarding SyncLink Consortium Package	2604	5/21/2003	JXB		
RX0789	Micron PowerPoint presentation slides, Micron DRDRAM Status	2604	5/21/2003	JXB		
RX0792	Email regarding Intel/Rambus meeting with DRAM suppliers week of 11/18	2604	5/21/2003	JXB		
RX0796						

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RX0819	IBMs License Policy Regarding Issues Related to Standards	2604	5/21/2003	JXB		
RX0824	Memorandum from Kenneth D. Grillo to Dr. M.J. Attardo and others re Rambus	2604	5/21/2003	JXB		
RX0825	Rambus Future PC Board Memory Requirements Preliminary	2604	5/21/2003	JXB		
RX0828	Tate letter to Appleton	6344	6/20/2003	6335:19; 6336:17; 6344:4; 63351:7; 6442:4; 6445:10; 6538:21; 6539:4; 6542:14; 6543:16		
RX0829	Email from Jeff Mailoux to Steve Appleton and others re Rambus Deal	2604	5/21/2003	6534:6; 6538:3,11; 6539:18; 6540:8; 6543:2; 6616:9; 6840:17; 6850:5; 6851:10; 6921:15		
RX0832	Email re Rambus	2604	5/21/2003	JXB		
RX0833	Email string	2604	5/21/2003	JXB		
RX0834	Mailloux - Appleton e-mail	2604	5/21/2003	JXB		
RX0836	Email string	2604	5/21/2003	6851:20; 6860:9; 6867:11		
RX0837		2604	5/21/2003	208:23	In Camera 4/29/2003 Order	
RX0839	Email from Sri Divakaruni to Jim Comfort and others re JEDEC Strategic Planning	2604	5/21/2003	JXB		
RX0841	1997 HEA Sales Strategy	2604	5/21/2003	JXB		
RX0844		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX0845	SyncLink Consortium Executive Meeting	2604	5/21/2003	6905:7		
RX0846	Email re: Meeting reports (SLDRAM and other companies)	2604	5/21/2003	JXB		
RX0847	Email re SyncLink Exec meeting in Yokohama	2604	5/21/2003	JXB		
RX0848	Document headed Intellectual Property Issues, SyncLink Consortium Confidential	2604	5/21/2003	JXB		
RX0849	SyncLink Consortium Executive Meeting, Yokohama, Japan	2604	5/21/2003	6906:20,21; 9044:18		
RX0850	E-mail regarding notes from Synclink meeting - Toyko, Japan	2604	5/21/2003	JXB		
RX0852	Email from Terry Lee to Gene Cloud regarding supplier comments	2604	5/21/2003	6937:25; 7046:5		
RX0855	Minutes of January 14 and 15, 1997 SyncLink Consortium Meeting	2604	5/21/2003	1253:20,21; 6935:24; 6942:1; 6944:2; 7045:5; 7050:12; 9049:20		
RX0856		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX0859	Handouts from JEDEC Special RAM Meeting	2604	5/21/2003	JXB		
RX0863		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	

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RX0864	Article entitled "Carpe DRAM Is Asis's dominance a memory?" in Tech Search	2604	5/21/2003			

#### **PUBLIC**

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RXUXXX	Minutes of Meeting No. 82, JC-42.3 Subcommittee on RAM Memories, 3/13-14/97, Ft. Lauderdale, FL	1087	5/7/2003	1064:5,9; 1086:24		
RX0890	Forward of message regarding Rambus.	2604	5/21/2003	6964:17,18		
RX0892	SDRAM II - DDR dated March 17, 1997	2604	5/21/2003	JXB		
RXDXG3	JEDEC MOS Memory Meeting report (GAP IN PRODUCTION)	2604	5/21/2003	4531:23; 4534:24		
RXU9UZ	Electronic Engineering Times, Op-Ed Speakout, Intel gambles on a sure DRAM thing	2604	5/21/2003	JXB		
RXN904	Intel Confidential document titled Rambus Program Review, 4/1/97	1086	5/7/2003	1059:22,25; 1086:19; 4803:7,8; 4807:7; 4808:22; 4810:7,8; 4818:6; 4821:5; 4824:8; 4825:5,6; 4828:15		
RXU9UA	Intel Confidential document titled Rambus Program Review, 4/1/97	2604	5/21/2003	JXB		
RX0906	Business card of Oliver Kiehl of SIEMENS	2604	5/21/2003	JXB		
RXU9UX	E-mail from Craig Hampel dated 4/9/97 re: DDR/JEDEC G2	2604	5/21/2003	JXB		
RX0910	Why SLDRAM? VLSI Technology, What Makes SLDRAM Comptell877g? (MEC50035/77/12018/03SORolSSING	G 227.04 5.4 TD -	-0.0571 Tc (2604)5	o.4 TD -0.0571 Tc (2604)5.4 TD -0.0571 Tc (2604	4)5.4 TD -0.0571	Tc (2604)5.4 TD -0

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RX1043	E-Mail re: 256M Application Specs	2604	5/21/2003	JXB		
RX1044	Rambus presentation slides titled Rambus Direct RDRAM Cost Reduction 11/9/97	2604	5/21/2003	JXB		
RX1045	[SLDRAM Inc Effort]	2604	5/21/2003	JXB		
RX1047	SLDRAM Consortium Confidential Minutes of 11/1-14/97 meeting, Yokohama, Japan	2604	5/21/2003	JXB		
RX1051	SLDRAM Consortium Executive Meeting Yokohama Japan 11/14/1997	2604	5/21/2003	JXB		
RX1056	[SLDRAM Consortium Budget Presentation]	2604	5/21/2003	JXB		
RX1058	Action Plan for 00/00/1998	2604	5/21/2003	JXB		
RX1059	Email from Joe Macri to Hans Wiggers and others re Dlls and other DDR issues	2604	5/21/2003	JXB		
RX1060	Email from Hans Wiggers to jmacri and others re Dlls and DDR issues	2604	5/21/2003	4695:5,9; 10580:7		
RX1065	E-Mail re: Minutes of SLDRAM Consortium Meeting 11/13/1997, Yokohama	2604	5/21/2003	JXB		
RX1066	Electronic Buyers' News, Technology Focus: High- Speed DRAMs, Executive Comment, DRAM Challenge: tracking transitions	2604	5/21/2003	JXB		
RX1073	Handwritten notes JC-42.5, Chairman: Mian Quddus/Samsung, Buttes Hotel, Tempe, AZ 12/11/97	2604	5/21/2003	JXB		
RX1073	Handwritten notes JC-42.5, Chairman: Mian Quddus/Samsung, Buttes Hotel, Tempe, AZ 12/11/97	9259	7/17/2003	9234:18; 9258:19,21; 9259:4,8		
RX1074	Email from Kenneth Grillo to Art Kilmer with attached Trip Report - SUN/IBM Meeting, 12/11/97	2604	5/21/2003	JXB		
RX1079	Forbes, Technology, Computers/Communications, Rambus: Moore's little helper in Silicon Valley a mere idea can be worth a billion dollars	2604	5/21/2003	JXB		
RX1080	Email	2604	5/21/2003	JXB		
RX1086		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	RX0807
RX1087	JEDEC MOS Memory Meeting	2604	5/21/2003	JXB		
RX1088	McGhee email to members of 42.3	2604	5/21/2003	JXB		
RX1091	RAMBUS BUSINESS PLAN	2604	5/21/2003	8174:16; 8175:10; 8303:13,25; 8304:4		
RX1092	SEMICONDUCTOR WEEKLY ROUNDUP - DDR DRAM MAY BE TOO LITTLE TOO LATE	2604	5/21/2003	JXB		
RX1094	Email from Terry Lee to Barry S. Basile re Estimates	2604	5/21/2003	JXB		CX2716
RX1095		2604	5/21/2003	6995:23; 6996:7; 7067:4	In Camera 4/29/2003 Order	

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RX1100	Email re SLDRAM membership	2604	5/21/2003	JXB	In Camera	
RX-1101		7074	6/24/2003	7073:17; 7074:1	4/29/2003 Order	
RX1102	Micron's Records Retention Policy	2604	5/21/2003	JXB		
RX1103	Ryan email	2604	5/21/2003	JXB		
	Email from J. Maillous to Farhad Tabrizi re Rambus					
RX1104	article with attached article titled Price Pressures	2604	5/21/2003	JXB		
	slow DRAM transitions					
RX-1105	Rambus Article	9110	7/16/2003	9053:16; 9110:16		
RX1109	Intel/Mitsubishi Enabling Meeting agenda issued to K. Hrima	2604	5/21/2003	JXB		
52/4444	Email dated March 4, 1998 Ford to Cartelli and	2024	5 /0.1 /0.000	1000 10		
RX1114	others re Semiconductor Biz News item FYI	2604	5/21/2003	1206:12		
RX1116	Orlando Meeting	2604	5/21/2003	JXB		
RX1117	E-mail regarding Orlando Meeting	2604	5/21/2003	JXB		
RX1119	E-mail Regarding Orlando Meeting	2604	5/21/2003	JXB		
	Lisenbee email to Lee, Schmidt and Heitzeberg					
RX1120	discussing test program for SLDRAM	2604	5/21/2003	JXB		
RX1121	EMail re: Invitation to High Performance DRAM PAG #11 on 03/18/1998	2604	5/21/2003	JXB		
RX1122	Email from Farhad Tabrizi to Terry Shoup ("Dean	2604	5/21/2003	JXB		
	Shoup") re: patent assignments to SLDRAM, Inc.					
RX1123		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1127	Memo from Ken McGhee to Gordon A. Kelly re: JC-42 Strategic Task Group	2604	5/21/2003	JXB		
RX1130	Email dated March 26, 1998 Vogley to Tabrizi re Slap Intel Now	2604	5/21/2003	JXB		
RX1133	Email from Gordon A. Kelley to Jim L. Rogers re JEDEC Strategic Task Group	2604	5/21/2003	2741:5		
RX1135	Future DRAM Task Group Meeting	2604	5/21/2003	JXB		
RX1137	Memorandum from Jim L. Rogers to Apps re March 2-6 1998 JEDEC Meeting Notes	2604	5/21/2003	JXB		
RX1138	Email dated April 3, 1998 McComas to Tabrizi re Rambus Strategy Seminar Monday April 13	2604	5/21/2003	9060:18		
RX-1139	Email dated April 3, 1998 McComas to Tabrizi re draft pitch for Rambus seminar	9110	7/16/2003	9063:2; 9110:17		
RX1146	Email string from Graham Allan to Betty Prince re Rambus Strategy Seminar Monday April 13, 1998	2604	5/21/2003	JXB		
RX1147	E-mail regarding RE: DRAM Futures 04/00/0000 Subcommittee Mtg	2604	5/21/2003	JXB		
RX1148	Email string from Betty Prince to Graham Allan regarding Rambus Strategy Seminar - 4/13	2604	5/21/2003	JXB		

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Exhibit #	<u>Description</u>	<u>Marked</u>	<u>Marked</u>	Mentioned/JX#/Notes	In Camera?	<u>Reference</u>
RX1149	Email from Desi Rhoden to Betty Prince and Farhad Tabriz re: Rambus Strategy Seminar - Monday April 13, 1998. "My, what a stir Bert has caused." Rambus and Intel attendance at seminar restricted.	2604	5/21/2003	9064:20		
RX1150	Hyundai market analysis	2604	5/21/2003	JXB		
RX1153	Email string from Farhad Tabrizi to Bert McComas re Rambus Strategy Seminar - Monday 4/13	2604	5/21/2003	JXB		
RX1155	Email from J. Mailloux to Farhad Tabrizi	2604	5/21/2003	9055:19		
RX1156	Rambus Strategic Review	2604	5/21/2003	JXB		
RX1157	Rambus Strategic Seminar	2604	5/21/2003	JXB		
RX1159	Bill Cochran; SLDRAM meeting notes	2604	5/21/2003	JXB		
RX1161	Email from Jim L. Rogers to Kenneth Grillo re April 14 Server DRAM Forum Notes with attached notes	2604	5/21/2003	JXB		
RX1162	Email from J. Mailoux to Steve Appleton and others re press release	2604	5/21/2003	6452:3,11		
RX1163	Handwritten notes titled Future DRAM Task Group, 4/16/98, Joe Macri, Silicon Graphics	2604	5/21/2003	JXB		
RX1164	Email dated April 17, 1998 Cartelli to McComas re SLDRAM	2604	5/21/2003	JXB		
RX1165	Email exchange between Gordon Kelley and others	2604	5/21/2003	JXB		
RX-1166	Email from Roberto Cartelli to Bert McComas re SLDRAM	9110	7/16/2003	9066:19; 9110:17		
RX1167	Email from Paul Coteus to Kenneth Grillo re SDF vs. JEDEC Future DRAM group	2604	5/21/2003	JXB		
RX1168	Email from Bill Hovis to Jim L. Rogers and others re IBM Confidential JEDEC Special Meeting in Mountain View, CA on Future DRAM Direction	2604	5/21/2003	JXB		
RX1170	Email to Infoguys from Jim L. Rogers re IBM Confidential: April 23 JEDEC DRAM TG Meeting Report	2604	5/21/2003	JXB		
RX1171	Correction to Tabrizi bio	2604	5/21/2003	JXB		
RX1174	Technical Summary of joint Meeting JEDEC DDR-II Task Group and Electronics Industry Association of Japan (EIAJ)	2604	5/21/2003	JXB		
RX1175	E-mail from Geoff Tate re: LGS exec meeting	2604	5/21/2003	JXB		
RX1176	InQuest Invoice No. 805271 dated 5/27/98	2604	5/21/2003	JXB		
RX1177		2604	5/21/2003	6517:7,11; 6556:2	In Camera 4/29/2003 Order	
RX1178	Email re May SLDRAM meeting summary	2604	5/21/2003	JXB		
RX1179	E-mail regarding JEDEC Legal Guides	2604	5/21/2003	JXB		

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RX1181	Summary of Business Trip to CA 06/00/1998	2604	5/21/2003	JXB	in Gamera: Reference
RX1183	Action Items and Conclusions	2604	5/21/2003	JXB	
RX1185	SLDRAM Executive Summit What Problem is Solved by SLDRAM?	2604	5/21/2003	7014:9; 7021:4	
RX1188	InQuest Powerpoint presentation slides headed SLDRAM Executive Summit. What Problem is Solved by SLDRAM?	8761	7/15/2003	8743:15; 9069:23	
RX1191	Meyer trip report & meeting minutes	2604	5/21/2003	JXB	
RX1193	Appleton e-mail to Sadler re DRAM Capital Spending	2604	5/21/2003	6514:23; 6515:2	
RX1195	Lee email analysis of Rambus shown at JEDEC meeting	2604	5/21/2003	JXB	
RX1196	E-mails between Bassuk and Perlman re SLDRAM patent applications	2604	5/21/2003	1227:23	
RX1197	E-mail Regarding Refresh First Showing	2604	5/21/2003	JXB	
RX1198	Hancock email regarding Rambus meeting with Compaq	2604	5/21/2003	JXB	
RX1202	SLDRAM Executive Summit Monterey CA 06/25/1998	2604	5/21/2003	7012:24; 9068:19	
RX1204	[SLDRAM New Memory Technology]	2604	5/21/2003	9071:23	
RX1206	E-mail Regarding DDR SDRAM Update	2604	5/21/2003	JXB	
RX1207	SLDRAM Executive Summit, Monterey	2604	5/21/2003	JXB	
RX1208	[Summary of Business Trip to CA 06/00/1998]	2604	5/21/2003	<b>RX11空25</b> ;h96744/22pp6ce.e.64 0 TD -0.0237 C Compaq	05e21; 9Tc (5/21/ 05e2 y 3t 58.Tc (82 0 TD -0.0

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RX1222	Email string from Mark Kellogg to WR Griffin Rambus 128Mb Chip Rotation	2604	5/21/2003			

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RX-1287	Compaq 1999 RDRAM Investment Proposal October 1998	2370	5/20/2003	2316:17,19; 2320:15; 2323:5; 2361:20; 2367:6; 2370:5		
RX1288	Micron press release re acquisition of TI's memory business	6537	6/20/2003	6505:8,14; 6537:10		
RX1289	Email dated October 1, 1998 Kim to Tabrizi re RDRAM Pricing forecast and strategy and document titled Executive Summary	2604	5/21/2003	JXB		
RX1289A	Email dated October 1, 1998 Kim to Tabrizi re RDRAM Pricing forecast and strategy and document titled Executive Summary	2604	5/21/2003	JXB		
RX1290	Email string from Bert McComas to Gil Russell re The other part of Intels DRAM monopoly strategy?	2604	5/21/2003	JXB		
RX1293	Email dated October 6, 1998 Tabrizi to Heller and others re RDRAM Pricing Forecast and document titled "Projected RDRAM ASP's"	2604	5/21/2003	9088:7		
RX1293A	Email dated October 6, 1998 Tabrizi to Heller and others re RDRAM Pricing Forecast and document titled "Projected RDRAM ASP's"	2604	5/21/2003	JXB		
RX1294	Micron press release re Intel \$500 million investment	2604	5/21/2003	6456:9,13		
RX1295	Email dated October 18, 1998 Kim to Martinez re Intel request for Hyundai Supply & competitive update	2604	5/21/2003	4970:10,14		
RX1296	Email from J. Mailloux to Terry Lee and others re Rambus thoughts	2604	5/21/2003	JXB		
RX1297	Fw Rambus Re Exec Meeting Next Week	2604	5/21/2003	JXB		
RX1298	Email dated October 23, 1998 from Min-Kwon Yoo re 10/29 Intel Meeting and document titled "WW44 (Oct. 29) Intel Meeting (Final)	2604	5/21/2003	JXB		
RX1299	Email from David Dorrough to Sam Chen re DDR M(9) Joint Meeting - 9/28	8761	7/15/2003	8757:13; 8761:12,16		
RX1302	Compaq Corporate Memory Procurement Memory Update November 1998	2604	5/21/2003	JXB		
RX-1302	Compaq Corporate Memory Procurement Memory Update November 1998	2370	5/20/2003	2324:5,9; 2363:17; 2367:21; 2370:12		
RX1303	New Challenges and New Business Models in the DRAM Market 11/00/1998	2604	5/21/2003	JXB		
RX1304	Email dated November 2, 1998 Tabrizi to Wang, and misc. charts	2604	5/21/2003	JXB		
RX1306	Email string from Joe Macri to Betty Prince and others re Future dram task group - meeting minutes for 9/18 and 10/12 meetings	2604	5/21/2003	4621:11; 4622:14; 4702:20,24; 4703:9,16		
RX-1308	U.S. Patent No. 5,835,956	6820	6/24/2003	5599:23; 6819:24		

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RX1311	LGS Proposal	2604	5/21/2003	JXB		

RX1312 LG Semicon Proposal to Intew (In Camera?) Tj ET 630 519.12 46.2 0.84 re f BT 709.56 531.720tR 0.8 (5/21/2003) Tj 58.8 03 TD -0.00865w (In thked) 4

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RX1443	Toshiba e-mail re MAI2 meeting report	5351	6/16/2003	5270:3; 5287:11; 5351:1		
RX1444	Email re I want my PC 133!!!	2604	5/21/2003	235:20; 6482:8,16; 6492:18; 7034:3		
RX1445	Re Heads Up Notes from Micron Baldwin, Don Call	2604	5/21/2003	4962:7,11; 6460:2,13		
RX1446	RAMBUS IN THE NEWS	2604	5/21/2003	JXB		
RX1447	[The Resurgence of Rambus DRAM]	2604	5/21/2003	JXB		
RX1449	Advanced Memory Internal an Industry Partnership	2604	5/21/2003	JXB		
RX1453	Emai from Patrick Gelsinger to Pete MacWilliams and others re Micron	2604	5/21/2003	4966:20,21; 4969:17		
RX1454	E-mail messages between Mark Ellsberry and Farhad Tabrizi	2604	5/21/2003	JXB		
RX1457	Email string from Dick Foss to to Jim Townsend and others re Mosaid DLL Patents	667	5/2/2003	663:22; 4713:15; 4715:23		
RX1457	Email string from Dick Foss to to Jim Townsend and others re Mosaid DLL Patents	2604	5/21/2003	JXB		
RX1458	Compaq Corporate Memory Procurement Rambus Update June 1999	2604	5/21/2003	JXB		
RX1459	Micron DRAM Update	2604	5/21/2003	JXB		
RX1461	Email string from Desi Rhoden to Jim Townsend and others re Mosaid DLL Patents	667	5/2/2003	JXB		
RX1461	Email string from Desi Rhoden to Jim Townsend and others re Mosaid DLL Patents	2604	5/21/2003	JXB		
RX1463	Rogers e-mail Electronic News	2604	5/21/2003	JXB		
RX1464	Micron press release re RDRAM samples	6480	6/20/2003	6359:18; 6360:11,25; 6363:9; 6455:8; 6457:3,11; 6480:10		
RX-1472	U.S. Patent No. 5,915,105	6820	6/24/2003	5615:6; 6820:9		
RX1474	Toshiba e-mail re AMI2 board meeting	2604	5/21/2003	JXB		
RX1475	Email from T.C. Chou to Sam Chen and others re AM12 Material on 6/23 with attached summary of patents	2604	5/21/2003	JXB		
RX1476	RAMBUS INC U.S. DIVISIONAL PATENT APPLICATIONS BASED ON PARENT APPLICATION SERIAL NO. 07/510,898	2604	5/21/2003	JXB		
RX-1479	U.S. Patent No. 5,917,760	6819	6/24/2003	5622:21; 6819:19		
RX1482	Rambus Strategic Review	2604	5/21/2003	JXB		
RX1484	AMI meeting minutes	2604	5/21/2003	JXB		
RX1487	Email from Farhad Tabrizi to schkim@sr.hei.co.kr re HEI LG RDRAM comparison	2604	5/21/2003	9099:20; 9111:9,18,24; 9113:11,21		
RX1488		7074	6/24/2003	7074:8,15	In Camera 4/29/2003 Order	
RX1492	Email re gross margins	2604	5/21/2003	JXB		

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RX1493		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1494	[Travel to Burlington for AMII Meeting Next Month]	2604	5/21/2003	JXB		
	Email from Jan du Preez to Everke Alexander and					
RX1500	others re Intel IDF: Dinner and RDRAM Implementers Forum	2604	5/21/2003	JXB		
RX1515	Re Notes from Meeting with Dr Lim Samsung	2604	5/21/2003	JXB		
RX1524	Email re Procurement support	2604	5/21/2003	JXB		
RX1525	Rambus presentation slides titled Rambus Cost Reduction Update 11/99	8736	7/14/2003	8725:21; 8733:20; 8734:7; 8735:24		
RX1528	IBM Confidential Re Intel Developing New DRAM Outside of JEDEC	2604	5/21/2003	JXB		
RX1532	Meeting Summary	4858	6/12/2003	4834:14; 4856:20; 4858:6		
RX1533	IBM Confidential Intel NMT Group	2604	5/21/2003	JXB		
RX1534	Email re Intel NMT	2604	5/21/2003	JXB		
RX1535	JEDEC Articles of Incorporation & Bylaws published	670	5/2/2003	667:8; 670:3		
RX1543	RDRAM Status Q4'99, IPED Memory Commodity, Intel Confidentialq	2604	5/21/2003	JXB		
RX1546	Email string from Kathy Garchow to Peter MacWilliams re Trying to recall re faster DRAM	2604	5/21/2003	4810:17		
RX1554	AM12 Meeting with Via, Tapei, 1/25/00 Email from Ken McGhee to JC-42.4 Non-Volatile	2604	5/21/2003	JXB		
RX1559	Memories re: Micron Patent Statement with attached 01/28/2000 letter from Lori Hickman of Micron to Gary smith of JEDEC	2604	5/21/2003	JXB		
RX1560	E-mail exchange between Sadler (Micron) and Blando (Dell)	2604	5/21/2003	JXB		
RX1565	Follow up to e-mail exchange between Sadler and Blando	2604	5/21/2003	JXB		
RX1568	Revised JEDEC Board of Directors Agenda Meeting No. 116, Sheraton Safari Report, Orlando, FL	2604	5/21/2003	JXB		
RX1569	Email re Micron JEDEC Board of Directors Meeting No. 116 list of	2604	5/21/2003	JXB		
RX1570	participants with Section D, "Disclosure on Patents Pending"	2604	5/21/2003	JXB		
RX1574	JEDEC Board Minutes Meeting No. 116, Orlando, FL	2604	5/21/2003	JXB		
RX1576	Draft meeting minutes from JEDEC Board of Directors Meeting No. 116 (Orlando, FL) with Stein's handwritten notes	2604	5/21/2003	JXB		
RX1581	Memo re Samsung pricing/production of RDRAM	2604	5/21/2003	JXB		

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RX1633		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1636 RX1638	Email string from Kevin Kettler to Jeff Clarke, Alan Luecke re Help on quote	2604	5/21/2003	JXB		

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RX1707	ADT Mtg 8.0 Munich Agenda 09/19/2000 to 09/20/2000 Rev 4 (FTC/SEC0058705 MISSING FROM PRODUCTION)	2604	5/21/2003	JXB		
RX1710		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1712	E-mail from Kelly to Rhoden, et al. (Desi, Howard, Art, Daniel, Richard, Any and Mike) regarding JEDEC Patent Guidelines. Guidelines attached.	1969	5/14/2003	1951:7; 1952:2,12; 1953:12; 1954:9,11,19; 1955:22; 1956:16; 1958:11; 1959:24; 1968:4; 1969:14; 1971:18; 1973:25; 1977:20; 2031:20; 2032:18; 2152:16; 2153:8,16		

RX1715

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RX1761	Intel Corporation document titled Memory Roadmap Update and Industry Status by Pete MacWilliams, Hidemori Inukai, Jon Kang, Shozo Saito	2604	5/21/2003	4915:14; 4916:6; 4930:18	
RX1762	Memory Roadmap Update & Industry Status	4981	6/12/2003	4931:4,14; 4934:5; 4935:17; 4936:9; 4937:13; 4939:2,22; 4940:9; 4942:18; 4980:24; 8712:13	
RX1764	Advanced Memory International Patent Portfolio Updated 03/00/2001	2604	5/21/2003	JXB	
RX1772	Patent Summary presentation	2604	5/21/2003	JXB	
RX1773	Email from Lisa Rhoden to Desi Rhoden and others re Patent Summary Presentation	2604	5/21/2003	1234:5; 5290:5	
RX1774	Coteus e-mail re invoice for AMI2 Membership dues	2604	5/21/2003	5258:4; 5280:24; 5289:17,23; 5299:7	
RX1778	E-mail re DRAM prices	2604	5/21/2003	JXB	
RX1784		2604	5/21/2003	JXB	In Camera 4/29/2003 Order
RX1786	ADT Meeting minutes of 03/20/01 mtg	2604	5/21/2003	JXB	4/23/2003 Oraci
RX1787	Fax cover from Clifford E. Wilkins, Jr. to Leslie Kimball and translation of 4/30/92 documents	2604	5/21/2003	JXB	
RX1792	Re Kentron Patent Status	2604	5/21/2003	4930:17	
RX1793	Re Kentron Patent Status	2604	5/21/2003	JXB	
RX1794	Re Kentron Patent Status	2604	5/21/2003	JXB	
RX1795	Re Kentron Patent Status	2604	5/21/2003	JXB	
RX1800		2604	5/21/2003	JXB	In Camera 4/29/2003 Order
RX1802	Re Kentrons Request to Join 1.2 Inches DDR LP DDR DIMM	2604	5/21/2003	JXB	<del></del>
RX1804	Memo from Clif Smith to EIA Executive Committee and Sector Presidents	2604	5/21/2003	JXB	
RX1806		2604	5/21/2003	JXB	RXC8044r63 and Sector President
					RX1806
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RX1820	JEDEC Interim Meeting 05/10/2001 to 05/11/2001 DDR II SDRAM	2604	5/21/2003	JXB		
RX1836		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1839	Email string from Steve Polzin to Levi Murray and Sam Patel re VLSI DRAM Rump Session	2604	5/21/2003	3973:22; 4021:18		
RX1842		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1848	Report entitled, "Desi's Ramblings," prepared for AM12 Marketing/Technical Meeting, June 21, 2001.	1291	5/7/2003	1275:23; 1291:6		
RX1851	Press Release "Jedec Announces Completion of Preliminary Spec For DDR II Memory and Starts Work on DDR III"	2604	5/21/2003	JXB		
RX1854	Preliminary DDR II SDRAM Specification Letter from Desi Rhoden to John Kelly and Ken	2604	5/21/2003	4733:15		
RX1858	McGhee at JEDEC re AMI Patents and Patent Applicationsemory and Starts					

RX18Tc 0.0425 Tw (Preliminary 25 Tw (Pre.M1 Cam2 (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (5/21/9 4v50X1842) Tj 268.68 0 T6068 TD - -0.04 RX18Tc 0.0425 Tw (Preliminary 25 Tw (Pre.M1 Cam2 (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (5/21/9 4v50X1842) Tj 268.68 0 T6068 TD - -0.04 RX18Tc 0.0425 Tw (Preliminary 25 Tw (Pre.M1 Cam2 (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (5/21/9 4v50X1842) Tj 268.68 0 T6068 TD - -0.04 RX18Tc 0.0425 Tw (Preliminary 25 Tw (Pre.M1 Cam2 (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (5/21/9 4v50X1842) Tj 268.68 0 T6068 TD - -0.04 RX18Tc 0.0425 Tw (Pre.M1 Cam2 (2604v50X1842) Tj 268.68 0 T6068 TD - -0.04 RX18Tc 0.0425 Tw (Pre.M1 Cam2 (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (5/21/9 4v50X1842) Tj 268.68 0 T6068 TD - -0.04 RX18Tc 0.0425 Tw (Pre.M1 Cam2 (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (5/21/9 4v50X1842) Tj 268.68 0 TD -0.04 Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (5/21/9 4v50X1842) Tj 268.68 0 TD -0.04 Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tj 63.12 0 TD -0.0374 Tc (2604v50X1842) Tj 268.68 0 TD -0.04 Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tc (2604) Tc (2604v50X1842) Tj 268.68 0 TD -0.0571 Tc (2604) Tc

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RX1884	Center Bond CSP for the Proposed JC-42 DDRII FBGA Standard Package Outline	2604	5/21/2003	JXB		
RX1885	Center Bond CSP for the Proposed JC42 DDRII FBGA Standard Package Outline	3345	5/28/2003	2845:12,17; 3134:14; 3345:7		
RX1886	-	2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1887		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1893	DDR II SDRAM Burst Length Option 09/12/2001 JC 42.3 Committee Item# 1293.04	2604	5/21/2003	JXB		
RX1894	Email from Kevin Ryan to K. Ryan and others re ADT update	2604	5/21/2003	JXB		
RX1895	Tessera Patent Assurance Letter	3345	5/28/2003	2845:12,17; 3134:14; 3345:7		
RX1896	JEDEC Meeting 09/10/2001 to 09/13/2001	2604	5/21/2003	JXB		
RX1897	JEDEC Handouts Questions	2604	5/21/2003	JXB		
RX1898		2604	5/21/2003	JXB	In Camera 8/26/2003 Order	
RX1899	Meeting Summary from Dynamic Burst Length Conference Call	2604	5/21/2003	JXB		
RX1900		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1901		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1905	Email from McGhee regarding Revised Interim meeting agenda	2604	5/21/2003	JXB		
RX1908		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1910	Re Notes from the JEDEC Interim Meeting 10/23/0000 to 10/24/0000	2604	5/21/2003	JXB		
RX1919		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1920	Micron's Supplemental Privilege Log	2604	5/21/2003	6974:19; 6975:3; 6975:8; 6977:15		
RX1922		2604	5/21/2003	6570:23	In Camera 8/5/2003 Order	
RX1922A	Email relating to DRAM pricing	2604	5/21/2003	JXB		
RX1925		2604	5/21/2003	JXB	In Camera 4/29/2003 Order	
RX1926	QMM Presentation 12/00/2002 Product Sales Policy	2604	5/21/2003	JXB		
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RX1931	JEDEC meeting minutes no. 101	9262	7/17/2003	9234:19; 9261:18; 9262:6,14		

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RX-2099-13	A 256Mb SDRAM Using Register-Controlled Digital DLL,"" A. Hatakeyama, et al. (Fujitsu), IEEE Journal of Solid State Circuits, Vol. 32 #11 Nov. 1997	9423	7/18/2003	9321:22; 9409:14; 9422:25; 9423:3		
RX2099-14	512 Mb DDR-II SDRAM datasheet (v0.1) HY5PS122423 & HY5PS12823, May 2002	2604	5/21/2003	JXB		
RX-2099-29	"High Speed Circuit Techniques in a 150 MHz 64M SDRAM," V. Lines et. al. (MOSAID) & IEEE International Workshop on Memory Technology, Design and Testing (MTDT), 1997	9422	7/18/2003	9321:20; 9408:17; 9422:20,23		
RX2099-39	"512Mb M-die DDR-II SDRAM Specification " v 0 14	2604	5/21/2003	JXB		
RX2099-43	"SyncLink Feasibility Report, v1.1SYNCLINK CONSORTIUM," MOSAID Technologies, Inc. April 30, 1996	2604	5/21/2003	JXB		
RX2099-45	Toshiba 4.5 Mbit RDRAM, TC59R0409K, Advanced Information (Feb. 1992); Toshiba TC59R0409 R-Megabit RDRAM, Technical Overview (1992, 1993)	2604	5/21/2003	JXB		
RX-2099-52						

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	"Royalty Rate Databook for Technology Licensing					
RX2105-006	Agreements," 4th Edition, Editors: Research Center	2604	5/21/2003	JXB		
KAZ 105-000	of Japan Institute of Invention and Innovation, Tables	2004	3/21/2003	JAB		
	21-1 and 21-2					
	IBM Standards Practices web page from					
RX2105-007	7 http://www.ibm.com.ibm/licensing/standards/practice	2604	5/21/2003	JXB		
	s.shtml					
	MOSAID: Patent Licensing Program from					
RX2105-008	8 http://www.mosaid.com/licensing/license_program.ht	2604	5/21/2003	JXB		
	m					
<b>5</b> 1/5/55 55/	MOSAID DRAM-based patenst from		- /- / /			
RX2105-009	9 http://www.mosaid.com//licensing/patent_families.ht	2604	5/21/2003	JXB		
DV0400	M	2024	E /0.4 /0.000	0707.44		
RX2106	ADT Meeting with Intel 01/09/2003	2604	5/21/2003	3787:11		
RX2120	Subpoena Duces Tecum to ATI Technologies, Inc.	2604	5/21/2003	JXB JXB		
RX2121	Micron chart entitled "Die Size Comparison"	2604	5/21/2003			
RX2123	Royaltystat.com royalty report	2604	5/21/2003	JXB	In Camera 7/9/2003	
RX2124		2604	5/21/2003	JXB	Order	
RX2137	[JEDEC Meeting Comments]	2604	5/21/2003	JXB	Oraci	
	Memory Strategies International Semiconductor					
RX2153	Memory Services document titled A Marketing Study	2604	5/21/2003	7815:17; 8998:18,23; 9002:12; 9005:22; 9008:12; 9014:19; 9030:1		
IXX2133	of the Rambus Dynamic RAM, Dr. Betty Prince					
RX2162	Dell Personal Systems Group RDRAM Outlook	2604	5/21/2003	JXB		CX2180
RX2163	Dell document titled Rambus Issues	2604	5/21/2003	JXB		
RX2183	Rambus Architectural Overview	8548	7/10/2003	8547:4; 8548:3		
RX2191	Amendments to SLDRAM Plus VC Kickoff Meeting at	2604	5/21/2003	JXB		
	Micron Site in Boise					
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	Channel Architecture	0004	E /04 /0000	IVD		
RX2202	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX2202A	Translated version of RX2202	2604	5/21/2003	JXB 1654:8		
RX2203 RX2204	Japanese document (Translation to be supplied)  Japanese document (Translation to be supplied)	1660	5/12/2003 5/21/2003	JXB		
RX2204A	Translated version of RX2204	2604 2604	5/21/2003	JXB		
RX2204A RX2205	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX2205A	Translated version of RX2205	2604	5/21/2003	JXB		
RX2205A	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX2206A		2604	5/21/2003	JXB		
RX2200A	Japanese document (Translation to be supplied)	2604	5/21/2003	JXB		
RX2207A	Translated version of RX2207	2604	5/21/2003	JXB		
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RX2297	E-mail regarding Conference call minutes from 5/29/02	2604	5/21/2003	JXB		
RX2298	E-mail regarding market review.	2604	5/21/2003	JXB		
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RX2300	Echelon ballot	2176	5/15/2003	2128:7; 2137:7,8; 2174:17,22; 2175:23		
RX2302	Article: "AMD Choses Direct RDRAM"	3931	6/2/2003	3818:16,20; 3813:20; 3819:8; 3931:16		
RX2303	E-mail to Steven Appleton from Geoff Tate (cc: Neil Steinberg, Gleoff Tate) action of the steinberg of the s	6479 6479:12	6/20/2003	6417:25; 6418:6; 6419:9; 6421:8; 6479:12		
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