

Appendix A

Glossary of Terms

Auto precharge: Auto precharge is an optional setting in DRAM read commands that eliminates the data stored in the DRAM in order to prepare for the next operation. It is a technology included on both SDRAM and DDR SDRAM.

BIOS: Basic Input / Output System. BIOS is built-in software that allows a computer to control the keyboard, display screen, disk drives, serial communications, and a number of miscellaneous functions without accessing programs from a disk.

Bus: The term bus refers to lines connecting devices in a computer, through which electrical signals are sent. A memory bus would be all of the lines that connect each memory device to the memory controller on the computer's motherboard.

Chipset / controller : The memory controller is generally part of the chipset. The memory controller is the device typically located on a motherboard of a computer, which controls communications between the CPU and the memory. The chipset also controls communications between the CPU and the other systems in the computer.

DDR SDRAM Standard: Double Data Rate SDRAM Standard. The DDR SDRAM Standard built upon many of the technologies already incorporated in the previous SDRAM Standard, and added new technologies to increase the speed and efficiency of the memory system. DDR SDRAM was developed during the mid to late 1990s at JEDEC and was adopted as a JEDEC standard in 1998. DDR SDRAM devices began ramping up in production in 1999 and in 2002 it overtook SDRAM as the predominant memory device.

DDR II SDRAM Standard: The DDR II Standard built upon many of the technologies incorporated in the previous SDRAM and DDR SDRAM Standards, and added more technologies to increase the speed and efficiency of the memory system. DDR II SDRAM was developed in the Future DRAM Task Group starting in April of 1998. DDR II devices began ramping up in production in late 2003 and are predicted to begin to replace DDR as the DRAM predominantly used in personal computers in 2004.

DRAM: Dynamic Random Access Memory. DRAM is a form of computer memory that is used in computers and other products. This form of memory is used to temporarily store digitally recorded information such that it is as available to be accessed when needed by the CPU. DRAM is "dynamic" because it needs to be refreshed every fraction of a second, otherwise the information is lost.

Dual bank design / multibank design: Dual bank design allows one bank of memory cells on a DRAM chip to be accessed while the other bank is preparing to be accessed. It is a technology included on both SDRAM and DDR SDRAM. RDRAM uses an advanced form of multibank

design with many more banks than JEDEC-compliant SDRAM or DDR SDRAM.

Dual edge clocking: Dual edge clocking means capturing data off both the rising and falling edges of the clock. This allows for twice the amount of data to be sent in each clock cycle compared to single edge clocking, where data is only sent on one edge of the clock. Dual edge clocking is a technology included on DDR SDRAM and on RDRAM.

EIA: The Electronic Industries Alliance (“EIA”) is a national trade association that represents all areas of the electronics industry. EIA is affiliated with JEDEC as well as with other associations.

Externally supplied reference voltage: Externally supplied reference voltage is the baseline voltage which is used to determine if a voltage signal is positive (one) or neutral (zero). If the voltage is higher than the reference voltage then the signal would be considered a one. If lower, it would be considered a zero. The advantage to externally supplied reference voltage is that it may provide a more accurate reference point for the signals transmitted. It is a technology included on RDRAM, SDRAM, and DDR SDRAM.

IEEE: The Institute of Electrical and Electronics Engineers (“IEEE”) is an organization that seeks to advance the theory and application of electrotechnology and allied sciences, serves as a catalyst for technological innovation and supports the needs of its members through a wide variety of programs and services since 1963. IEEE working groups sometimes work in conjunction with JEDEC subcommittees in developing certain standards.

JEDEC: The JEDEC Solid State Technology Association (once known as the Joint Electron Device Engineering Council), is the semiconductor engineering standardization body of the Electronic Industries Alliance (EIA). JEDEC has existed since 1960 and is the primary standard- setting organization at issue in the Rambus matter.

JC-42 Committee: The JC-42 Committee is a JEDEC committee, which focuses on Memory Devices. Jim Townsend was the chairman of this committee for the operative period.

JC-42.3 Subcommittee: The JC-42.3 Subcommittee is a JEDEC subcommittee of the JC-42 Committee, which focuses on RAM Memories. Gordon Kelley was the chairman of this subcommittee for the operative period.

Memory module: Memory module refers to the component which includes the DRAM chips and the small printed circuit board upon which the chips are placed. Memory modules are plugged directly into computers and memory upgrades to personal computers are performed by adding new memory modules to the computer.

MOST / Mosys: Mosys was a company that developed the MOST DRAM in the mid 1990s. The MOST DRAM was proposed proprietary memory solution, but it never achieved significant market penetration.

Motherboard: The motherboard is the printed circuit board upon which almost all important

components of a computer are fastened. Memory modules, CPUs, and video cards, among other components, are plugged in to the motherboards in personal computers.

Multiplexed bus: A multiplexed bus is a bus, whose lines are used for more than one function, during an operation. For example, each multiplexed bus line might carry address, command, and data information between the memory and the controller during a read or write operation.

Narrow / wide bus: A narrow bus refers to a bus that contains few lines relative to a wide bus. A traditional wide bus contains dedicated bus lines used to transmit only certain types of

the early 1990s. This technology allowed the DRAM industry to use standard DRAMs with four

and was adopted as a JEDEC standard in October 1993. Memory devices conforming to the SDRAM Standard began ramping up in production in the mid-1990s and by 1998, SDRAMs accounted for approximately 60% of DRAM revenue share.

SyncLink: SyncLink was a specification for another form of packet-based memory that was first proposed at the IEEE during the mid 1990s and later developed in the privately formed SyncLink Consortium. Later proposals were doubly multiplexed and packetized; they had shared command and address lines and separate, dedicated data lines. SyncLink never achieved significant market penetration.

System clock: A system clock in a computer consists of a series of electrical impulses that resemble sine waves. A system clock is used to ensure that the behavior of each component in the system (e.g., DRAMs and memory controllers) maintain their operations in an appropriate timing to all of the other components in the system.

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Appendix B

Witness Index¹

Steven Appleton: Chairman of the Board of Directors, Chief Executive Officer and President of Micron Technology, Inc. Mr. Appleton has been at Micron since 1991.

Andreas Bechtolsheim: Vice President and General Manager of Gigabit Systems at Cisco Systems, Inc. He co-founded Sun Microsystems, a company that builds UNIX servers and workstations and was Vice President of Technology. He also founded Granite Systems, a developer of gigabit switching products.

Henry Becker: Vice President and Managing Director of Infineon Technologies Richmond. Mr. Becker is in charge of the operations of Infineon's DRAM manufacturing plant in Richmond.

Reese Brown: Retired engineer who attended JEDEC meetings as a consultant to JEDEC in the 1990's.

Sam Calvin: Former Senior Staff Electrical Engineer at Intel Corporation. Mr. Calvin was a representative for Intel at JEDEC meetings from 1992-1997.

Samuel Chen: Mitsubishi's JEDEC representative throughout the 1990's.

Richard Crisp: Engineer who was Technology and Business Development Manager of the Asia-Pacific Region at Rambus. Mr. Crisp was the primary Rambus representative at JEDEC JC 42.3 meetings from May 1992 until Rambus withdrew from JEDEC.

William Davidow: Chairman of the Board of Directors at Rambus.

Anthony Diepenbrock: Patent attorney who joined Rambus as an in-house lawyer in September 1995 and remained until May 1999. He is now Of Counsel at Dechert LLP.

Mike Farmwald: Co-founder of Rambus and member of its Board of Directors.

Martin Fliesler: Founding partner of the intellectual property law firm of Fliesler, Dubb, Meyer & Lovejoy LLP in San Francisco, California.

Michael Geilhufe: Consultant and retired Intel executive.

¹ The information in this Witness Summary Index is substantially similar to the information found in the Joint Witness Index, which was filed pursuant to 16 C.F.R. § 3.46 on September 5, 2003.

Bob Goodman: Chief Executive Officer of Kentron Technologies, Inc, a company that provides memory solutions.

Jackie Gross: Director of Memory Central Direct Procurement at Hewlett-Packard Company. Ms. Gross was formerly the head of memory procurement, including DRAM, at Compaq. She supervised Compaq's JEDEC representative and is currently the supervisor of Hewlett-Packard's JEDEC representative.

Alan Grossmeier: Engineer at Cray, Inc, who has served as Cray's JEDEC representative since 1989.

David Gustavson: Secretary of the SyncLink Consortium in the mid-1990's who attended and took minutes of numerous SyncLink meetings.

Mr. Landgraf formerly was a DRAM Senior Procurement Engineer at Hewlett-Packard Company. Mr. Landgraf attended JC-42.3 subcommittee meetings on behalf of Hewlett-Packard from 1994-1999 and served on the JEDEC Council from 1998-1999.

Terry Lee: Executive Director of Advanced Technology and Strategic Marketing at Micron Technology, Inc. He was also a JEDEC participant for Micron from 1994-2002.

Joe Macri: Director of Technology at ATI Technologies, Inc, a company that provides graphics design to the computer industry. Mr. Macri has been a representative to JEDEC since 1997, presided as the former Chairman of the Future DRAM Task Group, and is the current chairman of JEDEC subcommittee JC-42.3.

Pete MacWilliams: Senior Fellow at Intel Corporation, and is also Director of Platform Architecture.

Jeffrey Mailloux: Executive at Micron Technology, Inc.

Preston McAfee: Murray Johnson Professor of Economics at the University of Texas, Austin.

Jim McGrath: Director of Strategic Product Development at Molex Connector Corporation. He has been a JEDEC representative on behalf of Molex since 1992.

Willi Meyer: Manager of Technical Marketing for Memory Products at Infineon AG. He was a JEDEC representative for Siemens, then later for Infineon since 1985 and acted as the primary representative for many years.

David Mooring: President Rambus and member of its Board of Directors.

Mark E. Nusbaum:

economics consulting firm that specializes in the economics of competition. Prior to joining NERA, Dr. Rapp was a professor at the State University of New York at Stony Brook.

Werner Reczek: Vice President and Managing Director at Infineon Technologies Austria AG in Villach, Austria.

Desi Rhoden: President and Chief Executive Officer of Advanced Memory International, Inc.

F: Complaint Counsel Proposed Findings of Fact
F: Complaint Counsel Reply Findings of Fact
Respondant Proposed Finding of Fact

in either party

<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if applicable)</u>
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Key

CCFF: Complaint Counsel Proposed Findings of Fact

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RRFF: Respondant Reply Finding of Fact

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
68	CCFF 707; RRF 707	
69	CCFF 710	RRFF 710
70	CCFF 711	RRFF 711
71	CCFF 713	RRFF 713
72	RFF 50*	CCRF 50
73	RFF 50*	CCRF 50
74	CCRF 50	
75	RFF 51*	CCRF 51
76	RFF 51*	CCRF 51
77	RFF 51*	CCRF 51
78	CCRF 51	
79	RFF 52	CCRF 52
80	RFF 53	CCRF 53
81	RFF 56*	CCRF 56
82	RFF 57	CCRF 57
83	CCFF 714	RRFF 714
84	CCFF 715	RRFF 715
85	CCFF 716	RRFF 716
86	RRFF 717	
87	CCFF 720	RRFF 720
88	CCFF 721	RRFF 721
89	CCFF 722	RRFF 722
90	CCFF 723	RRFF 723
91	CCFF 724	RRFF 724
92	CCFF 732*	RRFF 732
93	RRFF 732*	
94	CCFF 733	RRFF 733
95	RRFF 733	
96	CCFF 735*	RRFF 735
97	CCFF 739	RRFF 739
98	CCFF 740; RRF 740 *	
99	CCFF 741; RRF 741*	
100	CCFF 744	RRFF 744
101	RRFF 744	
102	RFF 606	CCRF 606
103	RFF 607	CCRF 607

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
142	RFF 604	CCRF 604
143	RFF 605	CCRF 605
144	RFF 648	CCRF 648
145	RFF 649-50	CCRF 649-50
146	RFF 651	CCRF 651
147	RFF 653	CCRF 653
148	RFF 654	CCRF 654
149	RFF 638	CCRF 638
150	RFF 639	CCRF 639
151	RFF 640	CCRF 640
152	RFF 641	CCRF 641
153	RFF 642	CCRF 642
154	RFF 643	CCRF 643
155	RFF 644	CCRF 644
156	RFF 645	CCRF 645
157	RFF 646	CCRF 646
158	RFF 647	CCRF 647
159	CCFF 746; RRFF 746	
160	CCFF 747	RRFF 747
161	CCFF 747; RRFF 747	
162	CCFF 1246*	RRFF 1248
163	CCFF 1249*	RRFF 1249
164	CCFF 1250; RRFF 1250	
165	CCFF 1252*	RRFF 1252
166	CCFF 756; RRFF 756	
167	CCFF 763; RRFF 763	
168	CCFF 725	RRFF 725
169	CCFF 729*	RRFF 729
170	CCFF 729*	RRFF 729
171	CCFF 729*	RRFF 729
172	CCFF 730*	RRFF 730
173	CCFF 730*	RRFF 730
174	CCFF 730*	RRFF 730
175	CCFF 730*	RRFF 730

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389	CCFF 590	RRFF 590
390	CCFF 591; RRFF 591	
391	CCFF 592	RRFF 592
392	CCFF 593	RRFF 593
393	RRFF 2369	
394	RRFF 2370	
395	~	
396	RRFF 2379	
397	CCFF 604	RRFF 604
398	CCFF 2370; RRFF 2370	
399	CCFF 610	RRFF 610
400	CCFF 611	RRFF 611
401	CCFF 612	RRFF 612
402	CCFF 613	RRFF 613
403	CCFF 614; RRFF 614	
404	CCFF 615	RRFF 615
405	CCFF 617	RRFF 617
406	CCFF 618; RRFF 618	
407	CCFF 619	RRFF 619
408	RRFF 620	
409	RRFF 621	
410	CCFF 622; RRFF 622	
411	CCFF 623; RRFF 623; RRFF 578	
412	CCFF 628	RRFF 628
413	RRFF 628	
414	CCFF 629; RRFF 629	
415	CCFF 633	RRFF 633
416	CCFF 637	RRFF 637
417	CCFF 638	RRFF 638
418	CCFF 639; RRFF 639	
419	CCFF 2332	RRFF 2332
420	CCFF 642	RRFF 642

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421	CCFF 643	RRFF 643
422	CCFF 644	RRFF 644
423	CCFF 645	RRFF 645
424	RRFF 646	RRFF 646
425	CCFF 647	RRFF 647
426	CCFF 648; RRFF 648	
427	CCFF 649	RRFF 649
428	CCFF 651	RRFF 651
429	CCFF 652	RRFF 652
430	CCFF 653	RRFF 653
431	CCFF 655	RRFF 655
432	RRFF 656	
433	CCFF 657; RRFF 657	
434	CCFF 658	RRFF 658
434	RRFF 658	
435	~	
437	CCFF 1501	RRFF 1501
438	CCFF 1502	RRFF 1502
439	CCFF 1503	RRFF 1503
440	CCFF 1504*; RRFF 1504	
441	CCFF 1505*	RRFF 1505
442	CCFF 1512	RRFF 1512
443	CCFF 1513	RRFF 1513
444	CCFF 1517	RRFF 1517
445	CCFF 1507	RRFF 1507
446	CCFF 1511	RRFF 1511
447	CCFF 1521	RRFF 1521
448	CCFF 1522 *	RRFF 1522
449	CCFF 1522 *	RRFF 1522
450	CCFF 1523	RRFF 1523
451	CCFF 1525	RRFF 1525
452	CCFF 1526*; CCFF 1527	RRFF 1526-27
453	CCFF 1528	RRFF 1528
454	CCFF 1529; RRFF 1529	

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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if applicable)</u>
	497	CCFF 1612 RRF 1612
	496	CCFF 1611 RRF 1611
	495	CCFF 1610 RRF 1610
	494	CCFF 1607 RRF 1607

493 CCFF 1606 RRF 1606

492 RRF 1606

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ALJ Finding		
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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if</u>
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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
596	~	
597	~	
598	CCFF 300*	RRFF 300
599	RRFF 301*	
600	~	
601	CCFF 347*	RRFF 347
602	CCFF 355*	RRFF 355
603	CCFF 355*	RRFF 355
604	RFF 199	CCRF 199
605	RFF 434*	CCRF 434
606	RFF 160*	CCRF 160
607	RRFF 308*	
608	CCFF 406*	RRFF 406
609	~	
610	RFF 115*; CCRF 115*	
611	~	
612	~	
613	~	
614	~	
615	~	
616	CCRF 319*	
617	~	
618	RFF 171*	CCRF 171
619	~	
620	~	
621	~	
622	~	
623	~	
624	~	
625	RFF 173	CCRF 173
626	RFF 173*; CCRF 173*	
627	RFF 118, 166-67	CCRF 118, 166-67
628	RRFF 308	
629	RRFF 418	
630	~	
631	~	

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
632	~	
633	~	
634	RFF 132	CCRF 132
635	RFF 134	CCRF 134
636	RFF 138	CCRF 138
637	CCRF 133	
638	RFF 133	CCRF 133
639	~	
640	RFF 209	CCRF 209
641	CCFF 438	RRFF 438
642	RFF 212	CCRF 212
643	~	
644	~	
645	CCRF 125*	
646	RFF 162*	CCRF 162
647	~	
648	CCFF 375	RRFF 375
649	~	
650	CCFF 380*	RRFF 380
651	RFF 134*	CCRF 134
652	CCFF 377*	RRFF 377
653	RRFF 320*	
654	~	
655	RFF 182*	CCRF 182
656	RFF 183	CCRF 183
657	~	
658	~	
659	RFF 123*	CCRF 123
660	CCRF 123*	
661	CCRF 123*	
662	~	
663	~	
664	~	
665	~	
666	CCFF 367*	RRFF 367
667	CCFF 368*	RRFF 368
668	CCFF 369*	RRFF 369
669	~	

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746	~	
747	~	
748	~	
749	CCFF 320*	RRFF 320
750	CCFF 320*	RRFF 320
751	~	
752	~	
753	~	
754	RRFF 331*	
755	~	
756	RFF 284	CCRF 284
757	RFF 285	CCRF 285
758	CCFF 335	RRFF 335
759	~	
760	~	
761	~	
762	~	
763	RRFF 339*	
764	~	
765	CCFF 340*	RRFF 340
766	~	
767	~	
768	~	
769	~	
770	~	
771	~	
772	RFF 219*	CCRF 219
773	~	
774	RRFF 301*	
775	RFF 275-78	CCRF 275-78
776	~	
777	RRFF 335*	
778	RFF 285	CCRF 285
779	RFF 294	CCRF 294
780	RRFF 333*	
781	RFF 427	CCRF 427
782	RFF 415*	CCRF 415
783	CCFF 342*	RRFF 342

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784	RFF 297*, 298*; CCRF 297*	
785	RFF 298*	CCRF 298
786	RFF 468	CCRF 468
787	RFF 469	CCRF 469
788	RFF 470	CCRF 470
789	RFF 471	CCRF 471
790	RFF 472	CCRF 472
791	RFF 473	CCRF 473
792	RFF 474	CCRF 474
793	RFF 475	CCRF 475
794	RFF 476	CCRF 476
795	RFF 477	CCRF 477
796	RFF 478	CCRF 478
797	RFF 479-80	CCRF 479-80
798	RFF 481	CCRF 481
799	RFF 482	CCRF 482
800	RFF 484*	CCRF 484
801	RFF 485*	CCRF 485
802	RFF 486	CCRF 486
803	RFF 487	CCRF 487
804	RFF 488; CCRF 488	
805	RFF 489	CCRF 489
806	RFF 490	CCRF 490
807	RFF 491*	CCRF 491
808	RFF 492; CCRF 492	
809	~	
810	CCRF 769*; RFF 771*	CCRF 771
811	RFF 497	CCRF 497
812	RFF 498	CCRF 498
813	RFF 499	CCRF 499
814	RFF 500	CCRF 500
815	RFF 501	CCRF 501
816	RFF 503	CCRF 503
817	RFF 504	CCRF 504
818	RFF 505	CCRF 505
819	RFF 506	CCRF 506

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820	RFF 507	CCRF 507
821	RFF 508	CCRF 508
822	RFF 509	CCRF 509
823	RFF 510	CCRF 510
824	~	
825	RRFF 312	
826	RFF 517	CCRF 517
827	RFF 518	CCRF 518
828	CCRF 519	
829	RFF 522	CCRF 522
830	RFF 523	CCRF 523
831	RFF 525	CCRF 525
832	RFF 526	CCRF 526
833	RFF 527	CCRF 527
834	RFF 528	CCRF 528
835	RFF 529	CCRF 529
836	RFF 530*	CCRF 530
837	~	
838	RFF 716*	CCRF 716
839	RFF 530	CCRF 530
840	RFF 533	CCRF 533
841	RFF 534	CCRF 534
842	RFF 536*; CCRF 536*	
843	CCRF 536*	
844	CCRF 536	
845	CCRF 536*	
846	CCRF 537*; CCRF 537*	
847	CCRF 537*	
848	RFF 538	CCRF 538
849	RFF 539*	CCRF 539
850	RFF 539*	CCRF 539
851	RFF 540	CCRF 540
852	RFF 541	CCRF 541
853	RFF 542	CCRF 542
854	RFF 543	CCRF 543
855	RFF 544	CCRF 544

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
969	RFF 399	CCRF 399
970	RFF 400	CCRF 400
971	RFF 401	CCRF 401
972	RFF 402	CCRF 402
973	RFF 403	CCRF 403
974	RFF 404	CCRF 404
975	RFF 405	CCRF 405
976	RFF 406	CCRF 406
977	RFF 407	CCRF 407
978	RFF 408	CCRF 408
979	RFF 410	CCRF 410
980	RFF 411	CCRF 411
981	RFF 412	CCRF 412
982	RFF 415	CCRF 415
983	CCFF 1718	RRFF 1718
984	CCFF 1720	RRFF 1720
985	CCFF 1721	RRFF 1721
986	RRFF 1720	
987	CCFF 1722	RRFF 1722
988	CCFF 1723	RRFF 1723
989	CCFF 1725	RRFF 1725
990	CCFF 1727	RRFF 1727
991	CCFF 1731	RRFF 1731
992	CCFF 1732	RRFF 1732
993	RRFF 1728*	
994	CCFF 1733	RRFF 1733
995	CCFF 1737	RRFF 1737
996	CCFF 1734	RRFF 1734
997	CCFF 1735	RRFF 1735
998	CCFF 1739	RRFF 1739
999	CCFF 1740*	RRFF 1740
1000	CCFF 1741	RRFF 1741
1001	CCFF 1742	RRFF 1742
1002	CCFF 1745	RRFF 1745
1003	CCFF 1746	RRFF 1746
1004	CCFF 1747	RRFF 1747
1005	CCFF 1748	RRFF 1748
1006	CCFF 1749	RRFF 1749

Key

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RFF: Respondant Proposed Finding of Fact

RRFF: Respondant Reply Finding of Fact

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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if applicable)</u>
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Key

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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if applicable)</u>
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Key

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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if</u>
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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
1152	RFF 827	CCRF 827
1153	RFF 828	CCRF 828
1154	RFF 829	CCRF 829
1155	RFF 830	CCRF 830
1156	RFF 831	CCRF 831
1157	RFF 832	CCRF 832
1158	RFF 833	CCRF 833
1159	RFF 834	CCRF 834
1160	RFF 835	CCRF 835
1161	RFF 836	CCRF 836
1162	RFF 837	CCRF 837
1163	RFF 838	CCRF 838
1164	RFF 839	CCRF 839
1165	RFF 840	CCRF 840
1166	RFF 843	CCRF 843
1167	RFF 844	CCRF 844
1168	RFF 845	CCRF 845
1169	RFF 846	CCRF 846
1170	RFF 847	CCRF 847
1171	RFF 848	CCRF 848
1172	RFF 850	CCRF 850
1173	RFF 851	CCRF 851
1174	RFF 852	CCRF 852
1175	RFF 853	CCRF 853
1176	RFF 854	CCRF 854
1177	RFF 855	CCRF 855
1178	RFF 856	CCRF 856
1179	RFF 859	CCRF 859
1180	RFF 860	CCRF 860
1181	RFF 861	CCRF 861
1182	RFF 862	CCRF 862
1183	RFF 863	CCRF 863
1184	RFF 865	CCRF 865
1185	RFF 866	CCRF 866
1186	RFF 867	CCRF 867
1187	RFF 868	CCRF 868
1188	RFF 871	CCRF 871
1189	RFF 872	CCRF 872

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
1190	RFF 873	CCRF 873
1191	RFF 874	CCRF 874
1192	RFF 875	CCRF 875
1193	RFF 876	CCRF 876
1194	RFF 877	CCRF 877
1195	RFF 878	CCRF 878
1196	RFF 879	CCRF 879
1197	RFF 880	CCRF 880
1198	RFF 881	CCRF 881
1199	RFF 882	CCRF 882
1200	RFF 883	CCRF 883
1201	RFF 884	CCRF 884
1202	RFF 887	CCRF 887
1203	RFF 889	CCRF 889
1204	RFF 890	CCRF 890
1205	RFF 891	CCRF 891
1206	RFF 892	CCRF 892
1207	RFF 895	CCRF 895
1208	RFF 896	CCRF 896
1209	RFF 897	CCRF 897
1210	RFF 898	CCRF 898
1211	RFF 899	CCRF 899
1212	RFF 900	CCRF 900
1213	RFF 901	CCRF 901
1214	RFF 902	CCRF 902
1215	RFF 903	CCRF 903
1216	RFF 904	CCRF 904
1217	RFF 907	CCRF 907
1218	RFF 908	CCRF 908
1219	RFF 909	CCRF 909
1220	RFF 910	CCRF 910
1221	RFF 911	CCRF 911
1222	RFF 912	CCRF 912
1223	RFF 913	CCRF 913
1224	RFF 914	CCRF 914
1225	RFF 916	CCRF 916
1226	RFF 917	CCRF 917
1227	RFF 918	CCRF 918

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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if applicable)</u>
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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding</u>
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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
1304	RFF 1020	CCRF 1020
1305	RFF 1021	CCRF 1021
1306	RFF 1022	CCRF 1022
1307	RFF 1025	CCRF 1025
1308	RFF 1026	CCRF 1026
1309	RFF 1027	CCRF 1027
1310	RFF 1028	CCRF 1028
1311	RFF 1029	CCRF 1029
1312	RFF 1030	CCRF 1030
1313	RFF 1031	CCRF 1031
1314	RFF 1032	CCRF 1032
1315	RFF 1033	CCRF 1033
1316	RFF 1034	CCRF 1034
1317	RFF 1035	CCRF 1035
1318	RFF 1038	CCRF 1038
1319	RFF 1039	CCRF 1039
1320	RFF 1040	CCRF 1040
1321	RFF 1041	CCRF 1041
1322	RFF 1042	CCRF 1042
1323	RFF 1045	CCRF 1045
1324	RFF 1046	CCRF 1046
1325	RFF 1047	CCRF 1047
1326	RFF 1048	CCRF 1048
1327	RFF 1049	CCRF 1049
1328	RFF 1050	CCRF 1050
1329	RFF 1051	CCRF 1051
1330	RFF 1052	CCRF 1052
1331	RFF 1054	CCRF 1054
1332	RFF 1055	CCRF 1055
1333	RFF 1056	CCRF 1056
1334	RFF 1057	CCRF 1057
1335	RFF 1058	CCRF 1058
1336	RFF 1060	CCRF 1060
1337	RFF 1062	CCRF 1062
1338	RFF 1063	CCRF 1063
1339	RFF 1064	CCRF 1064
1340	RFF 1065	CCRF 1065
1341	RFF 1066	CCRF 1066

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
1342	RFF 1067	CCRF 1067
1343	RFF 1070	CCRF 1070
1344	RFF 1071	CCRF 1071
1345	RFF 1073	CCRF 1073
1346	RFF 1074	CCRF 1074
1347	RFF 1075	CCRF 1075
1348	RFF 1076	CCRF 1076
1349	RFF 1077	CCRF 1077
1350	RFF 1078	CCRF 1078
1351	RFF 1079	CCRF 1079
1352	RFF 1080	CCRF 1080
1353	RFF 1081	CCRF 1081
1354	RFF 1082	CCRF 1082
1355	RFF 1083	CCRF 1083
1356	RFF 1084	CCRF 1084
1357	RFF 1085	CCRF 1085
1358	RFF 1086	CCRF 1086
1359	RFF 1089	CCRF 1089
1360	RFF 1090	CCRF 1090
1361	RFF 1091	CCRF 1091
1362	RFF 1094	CCRF 1094
1363	RFF 1095	CCRF 1095
1364	RFF 1096	CCRF 1096
1365	RFF 1098	CCRF 1098
1366	RFF 1099	CCRF 1099
1367	RFF 1100	CCRF 1100
1368	RFF 1101	CCRF 1101
1369	RFF 1102	CCRF 1102
1370	RFF 1103	CCRF 1103
1371	RFF 1106	CCRF 1106
1372	RFF 1107	CCRF 1107
1373	RFF 1108	CCRF 1108
1374	RFF 1109	CCRF 1109
1375	RFF 1110	CCRF 1110
1376	RFF 1111*	CCRF 1111
1377	RFF 1111*	CCRF 1111
1378	RFF 1112	CCRF 1112
1379	RFF 1113	CCRF 1113

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
1380	RFF 1114	CCRF 1114
1381	RFF 1115	CCRF 1115
1382	RFF 1118	CCRF 1118
1383	RFF 1119	CCRF 1119
1384	RFF 1120	CCRF 1120
1385	CCFF 2411	RRFF 2411
1386	RRFF 2411	
1387	RRFF 2411	
1388	RFF 1125	CCRF 1125
1389	RFF 1126	CCRF 1126
1390	RFF 1127	CCRF 1127
1391	RFF 1128	CCRF 1128
1392	RFF 1129	CCRF 1129
1393	RFF 1130	CCRF 1130
1394	RFF 1131	CCRF 1131
1395	RFF 1132	CCRF 1132
1396	RFF 1133	CCRF 1133
1397	RFF 1134	CCRF 1134
1398	RFF 1135	CCRF 1135
1399	RFF 1136	CCRF 1136
1400	RFF 1137	CCRF 1137
1401	RFF 1138*	CCRF 1138
1402	RFF 1139	CCRF 1139
1403	RFF 1148-1149	CCRF 1148-49
1404	RFF 1150	CCRF 1150
1405	RFF 1151	CCRF 1151
1406	RFF 1152	CCRF 1152
1407	RFF 1153	CCRF 1153
1408	RFF 1154	CCRF 1154
1409	RFF 1155	CCRF 1155
1410	RFF 1156	CCRF 1156
1411	RFF 1157*	CCRF 1157
1412	RFF 1157*	CCRF 1157
1413	RFF 1158	CCRF 1158
1414	RFF 1159*	CCRF 1159
1415	RFF 1159*	CCRF 1159
1416	RFF 1160	CCRF 1160
1417	RFF 1161	CCRF 1161

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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if applicable)</u>
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Key

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RRFF: Respondant Reply Finding of Fact

1457 RFF 1211

1458 RFF 1212

1459 RFF 1213

1460 RFF 1214

1461 RFF 1215

ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
1532	RFF 778	CCRF 778
1533	RFF 779	CCRF 779
1534	RFF 780	CCRF 780
1535	RFF 781	CCRF 781
1536	RFF 1365	CCRF 1365
1537	RFF 1366	CCRF 1366
1538	RFF 1367	CCRF 1367
1539	RFF 1368	CCRF 1368
1540	RFF 1369	CCRF 1369
1541	RFF 1370	CCRF 1370
1542	RFF 1372	CCRF 1372
1543	RFF 1373	CCRF 1373
1544	RFF 1374	CCRF 1374
1545	RFF 1375	CCRF 1375
1546	RFF 1377*	CCRF 1377
1547	RFF 1377*	CCRF 1377
1548	RFF 1378	CCRF 1378
1549	RFF 1379	CCRF 1379
1550	RFF 1380	CCRF 1380
1551	RFF 1381	CCRF 1381
1552	RFF 1382	CCRF 1382
1553	RFF 1383	CCRF 1383
1554	RFF 1384	CCRF 1384
1555	RFF 1385	CCRF 1385
1556	RFF 1386	CCRF 1386
1557	RFF 1392	CCRF 1392
1558	RFF 1393	CCRF 1393
1559	RFF 1395	CCRF 1395
1560	RFF 1396	CCRF 1396
1561	RFF 1398	CCRF 1398
1562	RFF 1399	CCRF 1399
1563	RFF 1400	CCRF 1400
1564	RFF 1402	CCRF 1402
1565	RFF 1403	CCRF 1403
1566	RFF 1404	CCRF 1404
1567	~	
1568	RFF 1406	CCRF 1406
1569	RFF 1407	CCRF 1407

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ALJ Finding	Based on	Corresponding Reply Finding (if applicable)
1608	RFF 1292	CCRF 1292
1609	RFF 1293	CCRF 1293
1610	RFF 1294	CCRF 1294
1611	RFF 1295	CCRF 1295
1612	RFF 1296	CCRF 1296
1613	RFF 1297	CCRF 1297
1614	RFF 1298	CCRF 1298
1615	RFF 1299	CCRF 1299
1616	RFF 1300	CCRF 1300
1617	RFF 1301	CCRF 1301
1618	RFF 1302	CCRF 1302
1619	RFF 1303	CCRF 1303
1620	RFF 1304	CCRF 1304
1621	RFF 1305	CCRF 1305
1622	RFF 1306	CCRF 1306
1623	RFF 1307	CCRF 1307
1624	RFF 1309	CCRF 1309
1625	RFF 1311	CCRF 1311
1626	RFF 1312	CCRF 1312
1627	RFF 1313	CCRF 1313
1628	RFF 1314	CCRF 1314
1629	RFF 1315	CCRF 1315
1630	RFF 1316	CCRF 1316
1631	RFF 1317	CCRF 1317
1632	RFF 1318	CCRF 1318
1633	RFF 1319	CCRF 1319
1634	RFF 1320	CCRF 1320
1635	RFF 1321	CCRF 1321
1636	RFF 1322	CCRF 1322
1637	RFF 1323	CCRF 1323
1638	RFF 1324	CCRF 1324
1639	RFF 1325	CCRF 1325
1640	RFF 1326	CCRF 1326
1641	RFF 1327	CCRF 1327
1642	RFF 1328	CCRF 1328
1643	RFF 1329	CCRF 1329
1644	RFF 1331	CCRF 1331
1645	RFF 1332	CCRF 1332

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<u>ALJ Finding</u>	<u>Based on</u>	<u>Corresponding Reply Finding (if applicable)</u>
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Key